

[Invited] Wide spectral response and highly robust Si image sensor technology

Rihito Kuroda and Shigetoshi Sugawa

Graduate School of Engineering, Tohoku University

6-6-11-811 Aza Aoba Aramaki, Aoba-ku, Sendai, Miyagi, 980-8579, Japan

Phone: +81-22-795-4833, E-mail:kuroda@fff.niche.tohoku.ac.jp

Abstract An image sensor technology based on flattened Si surface is summarized that enables a wide spectral response of 200-1000 nm and high robustness of light sensitivity toward ultraviolet light exposure. Performances and opportunities of the light detectors and image sensors using the developed technology are demonstrated.

Keywords: photodiode, spectral response, ultraviolet light, spectrometer

1. Introduction

Light sensors and image sensors with wide spectral response to UV/VIS/NIR waveband are employed in various types of analytical instruments and imaging instruments used in a variety of scientific and engineering fields [1-2]. Sensors used in these fields are required to have a high sensitivity for 200-1000 nm as well as a high robustness of light sensitivity and dark current toward strong UV-light irradiation. This paper describes an advanced wide spectral response and highly robust imaging technology based on flattened Si-surface for photodiodes, photodiode array (PDA) sensors and image sensors.

2. Developed technology and sensor performances

Fig. 1 shows the light penetration depth in Si as a function of wavelength. In order to achieve high quantum efficiency (QE) in Si for a wide waveband, the light sensitive region with regards to Si depth must range from a few angstrom from the light receiving surface to several tens of micron meter. Fig. 2 shows the cross sectional illustration of a CMOS image sensor pixel with the key developed structures for a wide spectral response and high robustness to UV-light exposure. A high concentration p^+ layer is formed with a steep dopant concentration profile on the flattened Si surface region. This structure improves the UV-light sensitivity while achieving a high robustness of light sensitivity toward the fixed charge and interface state generations due to UV-light exposure [3-5]. A relatively thick p -type epitaxial layer is utilized in order to enhance the NIR-light sensitivity. The process technologies to form this structure are explained in detail elsewhere [3-5]. Furthermore, recently the atomically flattening technology when applied to the gate insulator/Si interface was shown to be effective to reduce random telegraph noise of source follower transistors based on the statistical evaluation of over 100 K samples [6].

Photodiode array (PDA) sensors and a CMOS image sensor were fabricated based on a 0.18 μm 1P3M CIS process technology [7]. Fig. 3 shows the chip micrograph of the developed PDA. Two types of PD structures were formed, i.e., p^+np type with the developed technology and np type as a reference. Fig. 4 shows the PD structures and photo-electric conversion characteristics. A higher FWC is obtained for p^+np type due to the increased PD capacitance. A better QE characteristic and its robustness were obtained for p^+np type than np type.

Fig. 6 shows the schematic image of a typical detector of spectrometers and the conceptual image of one of the proposed structures of PDA. Here, to improve the light transmittance to Si, PDA light receiving regions are divided into several regions and a high transmittance optical layer corresponding to the receiving light waveband is formed above Si in each region. The concept was verified by evaluating the fabricated seven types of PDs

with different optical layers composed of only SiO_2 and SiN_x films as shown in Fig. 7 [8]. A very high QE is expected to be obtained by this PDA technology.

Fig. 8 shows the spectral sensitivity of the developed CMOS image sensor before and after UV-light exposure stress [4-5, 9-10]. High sensitivity is obtained for a wide spectral range and almost no degradation of light sensitivity occurs due to the strong light exposure stress that accounts for over 10 years-use. Fig. 9 shows some sample images captured by the developed CMOS image sensor under visible and UV-light illumination conditions. More information is obtained in the UV-light illumination condition than visible light case. This implies a usefulness of the UV-light imaging for the life-scientific and the agricultural applications.

3. Conclusion

In this paper, Si image sensor technologies featuring the PD pn junction and the optical layer above Si surface are summarized and their impacts on the spectral sensitivity and the sensitivity robustness and opportunities are demonstrated.

References

- [1] R. J. Robbins, S. R. Bean: "Development of a quantitative high-performance liquid chromatography-photodiode array detection measurement system for phenolic acids," *J. Chromatography A*, Vol.1038, pp.97-105 (June 2004).
- [2] X. Hou and B. T. Jones, R. A. Meyers ed.: "Inductively Coupled Plasma/Optical Emission Spectrometry," *Encyclopedia of Analytical Chem.*, pp.9468-9485, John Wiley & Sons Ltd, Chichester, (2000).
- [3] R. Kuroda, T. Nakazawa, K. Hanzawa and S. Sugawa: "Highly Ultraviolet Light Sensitive and Highly Reliable Photodiode with Atomically Flat Si Surface," *IISW*, pp.38-41 (June 2011).
- [4] R. Kuroda, S. Kawada, S. Nasuno, T. Nakazawa, Y. Koda, K. Hanzawa and S. Sugawa: "A Highly Ultraviolet Light Sensitive and Highly Robust Image Sensor Technology Based on Flattened Si Surface," *ITE Trans. MTA*, Vol.2, No.2, pp.123-130, (Apr. 2014).
- [5] R. Kuroda and S. Sugawa: "Si image sensors with wide spectral response and high robustness to ultraviolet light exposure," *Review paper, IEICE ELEX*, Vol.11, No.10, pp.20142004-1-16 (May 2014).
- [6] T. Goto, R. Kuroda, N. Akagawa, T. Suwa, A. Teramoto, X. Li et al.: "Atomically Flattening of Si Surface of Silicon on Insulator and Isolation-patterned Wafers," *Jpn. J. Applied Physics*, special issue of the 2014 SSDM, under review.
- [7] T. Akutsu, S. Kawada, Y. Koda, T. Nakazawa, R. Kuroda and S. Sugawa: "A 1024 \times 1 Linear Photodiode Array with Fast Readout Speed Flexible Pixel-level Integration Time and High Stability to UV Light Exposure," *SPIE/IS&T EI*, Vol.9022, pp.90220L-1-8 (Feb. 2014).
- [8] Y. Koda, R. Kuroda and S. Sugawa: "High quantum efficiency 200-1000 nm spectral response photodiodes with on-chip multiple high transmittance optical layers," *Proc. IEEE Sensors 2014*, pp.1664-1667 (Nov. 2014).
- [9] R. Kuroda, S. Kawada, S. Nasuno, T. Nakazawa, Y. Koda, K. Hanzawa and S. Sugawa: "A FSI CMOS Image Sensor with 200-1000 nm Spectral Response and High Robustness to Ultraviolet Light Exposure," *IISW*, pp.61-64 (June 2013).
- [10] S. Nasuno, S. Kawada, Y. Koda, T. Nakazawa, K. Hanzawa, R. Kuroda and S. Sugawa: "A wide dynamic range CMOS image sensor with 200-1100 nm spectral sensitivity and high robustness to UV light exposure," *Jpn. J. Appl. Phys.*, Vol.53, pp.04EE07-1-4, (Mar. 2014).

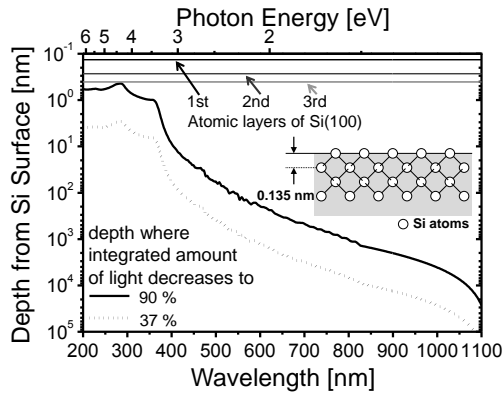


Fig. 1 Light penetration depth in Si as a function of wavelength.

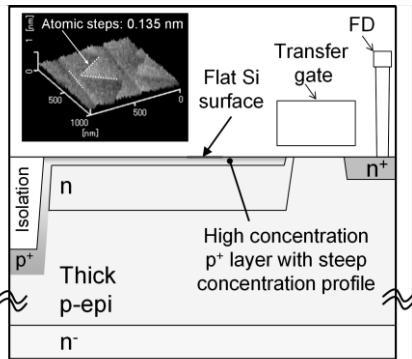


Fig. 2 Cross sectional illustration of a CMOS image sensor pixel with key structures to achieve the wide spectral response and the high robustness to UV-light exposure.

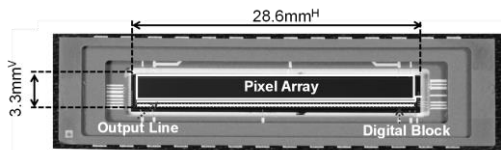


Fig. 3 Chip micrograph of developed PDA. Pixel size is $20\ \mu\text{m}$ by $2500\ \mu\text{m}$ and the number of pixels is 1024.

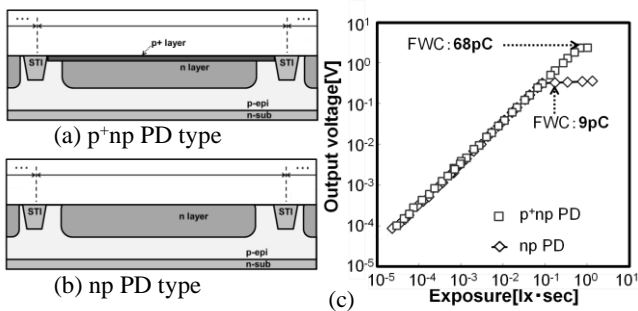


Fig. 4 (a-b) Cross sectional illustrations of fabricated PD types and (c) photo-electric conversion characteristics of the fabricated PDAs.

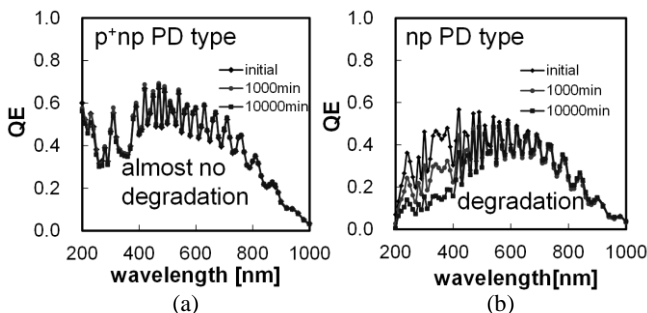


Fig. 5 Spectral QE for the developed PDAs before and after UV-light exposure stress. (a) p^+np PD type and (b) np PD type. The inter-metal layer SiO_2 film has $3.8\ \mu\text{m}$ thickness.

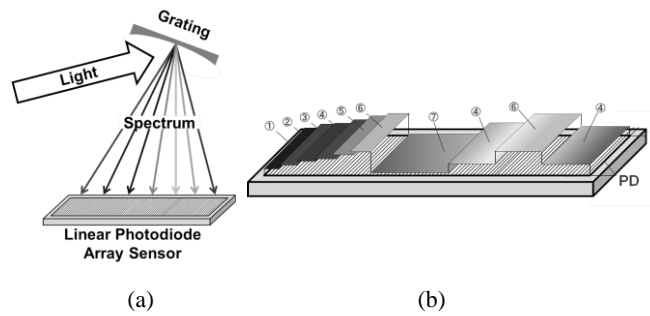


Fig. 6 (a) Schematic image of the typical detector of a spectrometer using PDA and (b) conceptual illustration of the proposed PDA structure with optimum optical layers for multiply divided pixel regions.

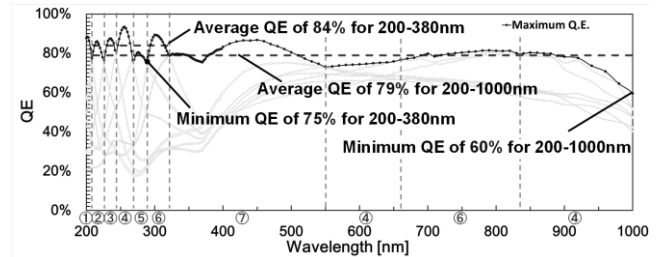


Fig. 7 QE as a function of wavelength for the seven types of PDs with different optical layers above Si formed by SiO_2 and SiN_x films only (gray lines) and the expected QE performance (black line) for the proposed PDA structure in Fig.6 (b).

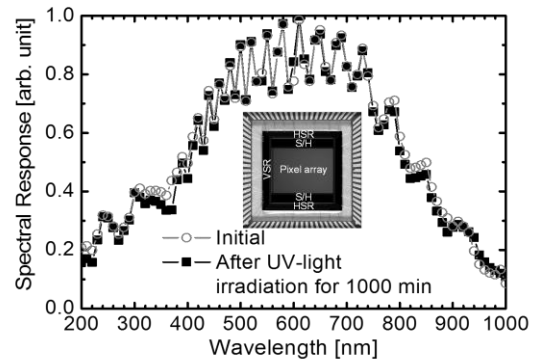


Fig. 8 Spectral response as a function of wavelength for the developed CMOS image sensor before and after UV-light exposure stress. The pixel pitch is $5.6\ \mu\text{m}$ and the number of pixels is $1280^H \times 960^V$.

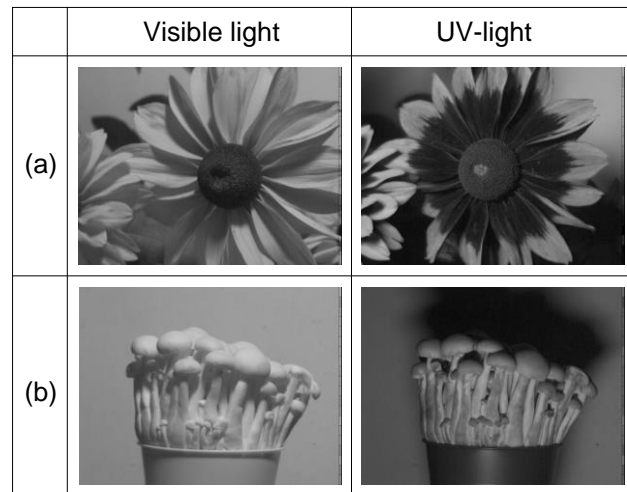


Fig. 9 Sample images of (a) Rudobekia flower and (b) Shimeji mushroom captured by the developed CMOS image sensor under visible light source and UV-light source ($360\ \text{nm}$).