A 3-Tap Global Shutter 5.0m Pixel with Background Canceling for 165MHz Modulated Pulsed Indirect Time-of-Flight Image Sensor

Masafumi Tsutsui^{1*}, Toshifumi Yokoyama¹, Takahisa Ogawa¹, Ikuo Mizuno¹, Nobuyoshi Takahashi¹, C. Ma², F. Wang³, J. Debondt⁴, J. Bogaerts⁴, X. Wang² & Assaf Lahav⁵

Introduction

Depth sensing is big incoming application of CMOS Image Sensors [1-4]. Pulsed indirect time-of-flight (TOF) [1] is one of the methods to measure depth by reflected light pulse from distant objects. In this method, high modulation frequency is desirable to increase signal-to-noise ratio (SNR) by summing up signals from multiple input light pulses. Additional improvement of SNR can be achieved by suppression of parasitic light sensitivity (PLS) and background cancelling [5]. In this paper, we report on our approaches to reduce PLS and to enhance pixel response to near-infra-red (NIR) light for high modulation frequency in 3-tap global shutter (GS) 5.0um pixel based on GS technology in 65nm CIS [6] and stacked process with backside deep trench isolation (BDTI).

Stacked Process

2.5um-pitch hybrid bonding process with metal-metal and dielectric-dielectric bonding is adopted for the pixel fabrication. Uniform operation of pixels in the whole array with high modulation frequency is achieved by In-pixel hybrid bonding connections. Figure 1 (a) shows cross-sectional view of hybrid bonding. Square arrangement of the metal-metal pad of hybrid bonding between 4-layers-metal sensor chip and 7-layers-metal logic chip is achieved. As shown in Figure 1 (a), each metal pad has only one via to the metal layers under the pads. This small pad-via connection makes it possible to utilize minimum line/space of the metal layers under the pad, and contributes increases of flexibility of routings of metal layers in the stacked process. Figure 1 (b) shows cumulative frequency of resistance per connection of 0.7k chain connection TEG in the whole wafer from multiple lots. Tight distribution and good connectivity without defects are achieved.

Pixel Schematic and Operation

Pixel schematic and operation timing are shown in Figure 2. Each tap (MG1-3) has a dedicated storage node (SN1-3) to store electrons for measuring depth in one frame. Two taps (MG1/2) are assigned to capture returned signal and to measure depth by the difference of signals of each taps, and the remaining tap (MG3) captures background signal for canceling in the period without returned signal. Charges in the SNs are transferred to one single floating diffusion to suppress noises from fluctuation of conversion gains and to save areas. Since SNs are pinned memory nodes by negatively biased gate (SG) [3], true correlated double sampling operation is enabled. In the readout period of signal of SNs, parasitic signal in photodiode is drained by DRN-transistor.

Figure 3 shows cross-sectional SEM view of fabricated pixels. 6um-thick-epi and 4um-depth BDTI are adopted. Metal reflector is placed under the photodiode. Both are designed to contribute to enhance quantum efficiency (QE) of NIR. The metal layer is placed as grid arrangement on the backside of sensor chip to reduce PLS into SNs.

¹ Tower Partners Semiconductor Co. Ltd., 800 Higashiyama, Uozu City, Toyama, 937-8585, Japan

² Gpixel INC, Yingkoulu 588, Changchun, China

³ Orbbec Shenzhen, Gaoxin South 7th Rd, Shenzhen, China

⁴ Gpixel NV, Mechelsesteenweg 271, Antwerp, Belgium

⁵ Tower Semiconductor, Migdal Haemek, Israel

^{1*} E-mail address: [tsutsui.masafumi@tpsemico.com,](mailto:tsutsui.masafumi@tpsemico.com) tel +81-80-4603-4174

Approaches

SN1-3 are placed closely to equalize PLS of each SN for maximizing effectiveness of background canceling as shown in figure 4. BDTI is also introduced around photodiode. Figure 5 shows simulation results of returned light profile with oblique angle. Incident light is reflected by BDTI and prevented from being irradiated into SNs.

Fast response to NIR consists of 1) vertical transfer in thick epi and 2) lateral transfer into each tap as shown in Figure 6. In this pixel, all the photo-generated electrons in the photodiode is collected at the position A in Figure 6 by the vertical field in the photodiode. Then electrons transfer into each MG1/2/3.

Figure 7 illustrates electrostatic potential profile of process A/B having different vertical field. In Process B, additional n-type implants are applied in shallow and deep regions of photodiode to make electrostatic potential deeper for increasing electric field. Results of pixel response of MG1/2 are shown in Figure 8. In the measurement, phase between the returned pulsed light and the starting time of modulation of MG1/2/3 is varied, and signals of each SN1/2/3 are measured. As shown in figure 8, Process B shows smaller background signal of MG2 due to incomplete transfer of electrons of MG1 compared to Process A. Fast transfer in the photodiode is achieved in Process B thanks to stronger vertical electric field. Stable background signal of Process B due to PLS also make the canceling by MG3 effective.

Lateral fields to MG1/3 are increased by layout design of n-type implant in the front of each MG, because MG1/3 have longer transfer path from position A shown in figure 6 compared to that of MG2. Figure 9 shows comparison of simulation results of electric field and measured demodulation contrast (DC) at 165MHz, 940nm wavelength. In optimized layout, difference of DC between each tap becomes smaller due to fast transfer assisted by larger electric field to MG1/3.

Conclusion

3-tap global shutter 5.0um pixel with background canceling for pulsed indirect TOF image sensor has been developed. 2.5um-pitch hybrid bonding is used for stacking sensor and logic wafers. PLS is suppressed by 4um-depth BDTI, and 6um-thick-epi is used to enhance QE at 940nm wavelength. To enhance pixel response for high modulation frequency of pulsed light, electric field in photodiode is carefully designed along all the transfer path. Demodulation contrast of 81% at 165MHz without background canceling is achieved. Figure 10 shows sample of depth sensing. Performances are summarized in table 1.

References

- [1] S. Kawahito et al., "A CMOS time-of-flight range image sensor with gates-on-field-oxide structure", IEEE Sensors J., vol. 7, no. 12, pp. 1578–1586, Dec. 2007.
- [2] Boris Rodrigues, et. al, "Indirect ToF Pixel integration fast buried-channel transfer gates and gradual epitaxy, and enabling CDS". IISW, 2019.
- [3] T. Jung et al., "A 4-tap global shutter pixel with enhanced IR sensitivity for VGA time-of-flight CMOS image Sensors", IS&T International Symposium on Electronic Imaging 2020.
- [4] Y. Kato, et. al, "320x240 Back-illuminated 10um CAPD pixels for high speed modulation Time-of-Flight CMOS image sensor", VLSI 2017.
- [5] S. Han et al., "A Time-of-Flight Range Image Sensor With Background Canceling Lock-in Pixels Based on Lateral Electric Field Charge Modulation", IEEE J. Electron Devices Soc., vol. 3, no. 3, pp.267-275, May 2015.
- [6] T. Yokoyama, et al., "High Performance 2.5um Global Shutter Pixel with New Designed Light-Pipe Structure", Proc. of 2018 International Electron Devices Meeting, San Francisco, USA.

Figure 1 a) Cross-sectional view of hybrid bonding and b) Cumulative frequency of resistance of hybrid bonding.

Figure 2 Pixel schematic and operation timing

Figure 4 Schematic of top-view of pixel.

Figure 3 Cross-sectional SEM view of fabricated device with 65nm stacked process

Figure 5 FDTD simulation result of pixel with 15 degrees returned light of 940nm wavelength

a) Side-view

Figure 6 a) Side-view and b) top-view of electrostatic potential profile of photodiode. Dotted line shows transfer path of electrons. Position A where photo-generated electrons reaches at the surface after travelling in photodiode is also shown.

Figure 8 Pixel response of MG1/2 of process A/B of 6um-thick epi.

Figure 10 Target (left), Depth image (mid) and 3D point cloud (right) captured by fabricated iTOF sensor

Figure 7 Illustrated electrostatic potential profile of process A/B in photodiode

Position A and MG1 are drawn in figure 6.

Figure 9 Comparisons of simulated lateral electric field toward MG1 and measured demodulation contrast with 165MHz, 940nm wavelength in layout optimization.