

# Towards Infrared Spectral Extension of CMOS Image Sensors

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## Abstract

A new structural integration scheme for p-type infrared (IR) absorbers directly on silicon (Si)-based CMOS image sensors with type-II band alignment is proposed and explored by calculation as an alternative to HgCdTe and other hybridized IR detectors. While HgCdTe is a material of choice for many IR detectors, its challenging manufacturing process and thermal reliability, among other factors, prove detrimental to some applications. Si-based sensors with directly deposited IR absorbing layers may be a suitable alternative. The band structures for the IR absorbing materials  $\text{Ge}_{0.89}\text{Sn}_{0.11}$  and  $\text{In}_{0.1}\text{Ga}_{0.9}\text{Sb}$  on Si are calculated and analyzed. Detector parameters including dark current, target wavelength, quantum efficiency (QE), and others, are also calculated to explore this approach in concert with separate experimental fabrication and measurement.

Keywords—CMOS image sensor, Non-visible, Spectral extension, IR.

## Introduction

There is a desire to transition towards Si-based IR sensors due to easier readout, potentially lower readout noise, and other circuit integration on-chip. Further, they are expected to avoid the manufacturing, scalability, and thermal reliability challenges HgCdTe and other hybrid sensors face. However, without an IR absorption layer, Si has a weak IR response because of its 1.12 eV bandgap [1]. IR imaging has many uses, including in light detection and ranging, security, medical, and more.

A new structural integration scheme for p-type infrared (IR) absorbers directly on silicon (Si)-based CMOS image sensors (CIS) with type-II band alignment is proposed and explored by calculation as an alternative to state-of-the-art hybrid IR detectors. In the proposed sensor, photons strike a p-type IR absorption layer that forms a type-II heterostructure on Si, generating photoelectrons that diffuse into the pixel's n-type Si storage well, leaving the holes in the p<sup>+</sup> Si pinning region. Depositing the IR absorber directly on silicon negates the need for metal hybrid bonds or bump bonds typically used in many IR detectors. Aside from reliability concerns during thermal cycling and yield, a metal interconnect implies a 3-T type readout with residual kTC noise and potential lag. Direct injection of photoelectrons into Si permits low-noise 4-T readout.

$\text{Ge}_{0.89}\text{Sn}_{0.11}$  and  $\text{In}_{0.1}\text{Ga}_{0.9}\text{Sb}$  are considered as possible materials for the IR absorption layer. Like HgCdTe, GeSn has a direct, tunable bandgap, but is CMOS- and Si-compatible,

and thus more scalable [2]–[4]. Experimental results in [5] verify proof-of-concept with 100 mA/W responsivity at  $\sim 2 \mu\text{m}$  compatible with back-end-of-line CMOS processing. III-V materials like InGaSb also have direct bandgaps [6] and are easier to manufacture than II-VI compounds [7].

To advance this approach towards use on a CIS, the electron diffusion and subsequent readout is first optimized via an interdigitated structure. Deposition layer thickness, minimum interdigitated structure width, target wavelength, quantum efficiency, and dark current are then calculated to further characterize the proposed device.

## Band Structure Calculation and Analysis

Some photoelectrons will diffuse to locations that won't be readout if the IR absorber is deposited over the entire p<sup>+</sup> Si pinning layer commonly used in frontside-illuminated devices. Therefore, an interdigitated Si layer with p<sup>+</sup> and n type regions, equivalently a partial pinning layer, is considered as shown in Figure 1. This can be implemented with an interdigitated mask for p<sup>+</sup> pinning layer implantation.

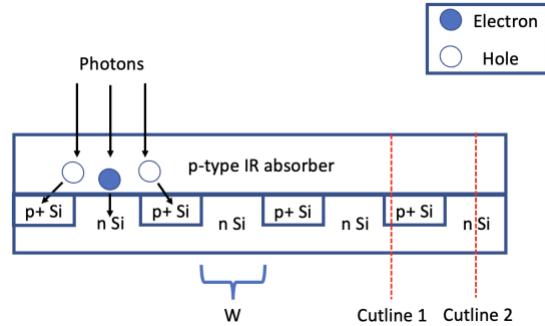


Figure 1. IR-absorbing layer on interdigitated silicon layer.

The IR-absorber/Si band structures are calculated using the Anderson approach, which utilizes the vacuum level to find the valence band offset. The difference in conduction band energies  $\Delta E_c$  is the difference between the Si and IR absorbers' affinities,  $q\chi_{\text{Si}}$  and  $q\chi_{\text{absorber}}$ , respectively. Using the direct bandgap of the IR absorber and indirect bandgap of Si (1.12 eV), we find the difference in IR absorber and Si bandgaps  $\Delta E_g$  and thus valence band energies  $\Delta E_v$ :

$$\Delta E_v = \Delta E_g - \Delta E_c \quad (1)$$

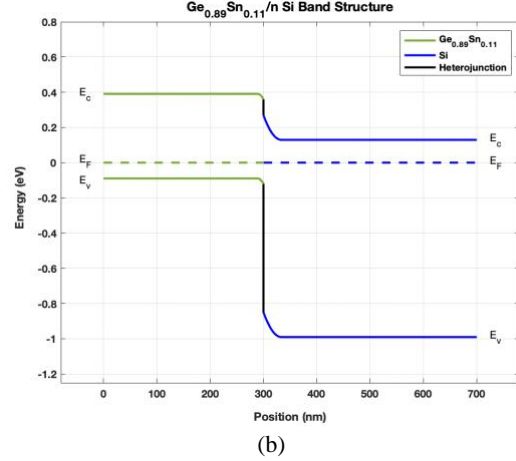
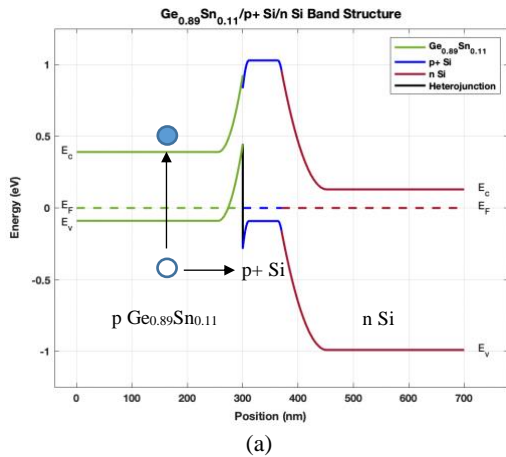
Assuming Si's vacuum level is  $q\chi_{Si} = 4.05$  eV, since  $E_c = E_v + E_g$ , we can determine Si's  $E_c$  and  $E_v$ , and use  $\Delta E_v$ ,  $\Delta E_c$ , and  $\Delta E_g$  to find those of the absorbers.

To calculate the Fermi level  $E_F$ , the density of states in the conduction band  $N_c$  and valence band  $N_v$  are first found using (2.13a) and (2.13b) on pp. 51 in [8]. The intrinsic Fermi level  $E_i$  and intrinsic carrier concentration  $n_i$  are then determined via (2.36) on pp. 62 and (2.21) on pp. 55 in [8], respectively. Finally,  $E_F$  is calculated using 2.38a and 2.38b on pp. 63 in [8].

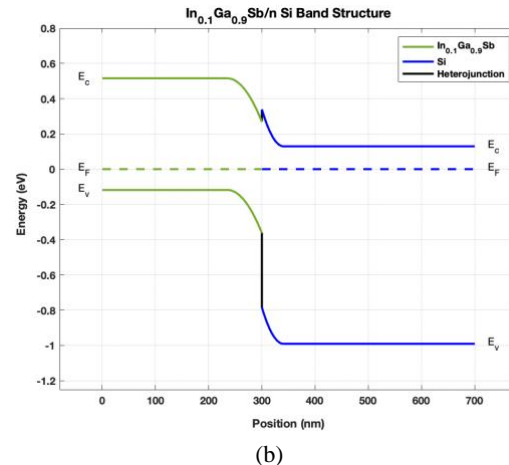
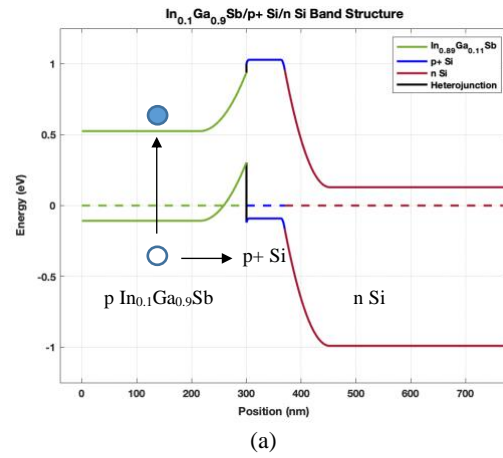
The depletion region widths on each side of the junction are found next using (143a) and (143b) in [9]. The Debye lengths  $L_D$  are subsequently determined and compared to the depletion widths to validate the analyses.

The band bending present in the IR absorbers and Si are found next. The sum of the interface potentials for the absorber and Si,  $q\psi_{absorber} + q\psi_{Si} = q\psi_{bi}$  and with 0 V applied, are related by (15a) and (15b) on pp. 82 of [9]. The Fermi levels of the materials are then aligned at 0 eV for simplicity.

The equilibrium band diagrams for the crosscuts of Fig. 1 were calculated with the results using  $Ge_{0.89}Sn_{0.11}$  and  $In_{0.1}Ga_{0.9}Sb$  shown in Fig. 2 and 3, respectively. As illustrated, the p+ Si regions in Figure 2a and 3a serve as a potential barrier against photoelectron transport but collect holes generated in the IR absorber. Conversely, the p-type IR absorber/n-type heterostructure allows electron diffusion to the n-well, as depicted in Fig. 2b and 3b. The  $Ge_{0.89}Sn_{0.11}/n$  Si conduction band structure has no barrier for electron diffusion from  $Ge_{0.89}Sn_{0.11}$  to n Si, while the  $In_{0.1}Ga_{0.9}Sb/Si$  conduction band has a small barrier that allows electrons to tunnel to the n Si well.  $Ge_{0.89}Sn_{0.11}$  is thus more suited to this design as an IR absorbing layer.



**Figure 2.** Calculated band structure for  $Ge_{0.89}Sn_{0.11}$  for (a) Cutline 1 and (b) Cutline 2 in Figure 1.



**Figure 3.** Calculated band structure for  $In_{0.1}Ga_{0.9}Sb$  (a) Cutline 1 and (b) Cutline 2 in Figure 1.

## QE Estimation

Deposition layer thickness, minimum interdigitated structure width, target wavelength, and QE are evaluated next to obtain a greater understanding of the device.

The ideal deposition thickness of the materials are first found using Beer-Lambert's law [10]

$$I = I_0 e^{-\alpha z} \quad (2)$$

where  $z$  is the material's depth,  $\alpha$  is the absorption coefficient, and  $I_0$  is the initial intensity.

The ratio of the amount of light absorbed and readout to the total amount of light incident on the material, or external quantum efficiency (EQE), is

$$EQE = A \times CP \quad (3)$$

where  $A$  is the amount of incident light absorbed in the device and  $CP$  is the collection probability, or probability that the light will be readout and is given by 8.13 in [11] as

$$CP = e^{-x/L_D} \quad (4)$$

where  $x$  is the distance from the depletion region and  $L_D$  is the minority carrier diffusion length. Light absorbed within the depletion region has a 100% probability of being collected in the absence of a potential barrier. However, the  $CP$  for light outside the depletion region exponentially decreases with distance from the depletion region. As a result,

$$EQE = A_{out} \times CP + A_{depl} \quad (5)$$

where  $A_{out}$  and  $A_{depl}$  are the percentages of light absorbed outside and inside of the depletion region, respectively.  $A_{depl}$  is given by

$$A_{depl} = e^{-\alpha(z-w_{depl})} - e^{-\alpha z} \quad (6)$$

with no potential barrier present. In the presence of a potential barrier,  $A_{depl}$  becomes  $A_{depl} \times T$ , the tunneling probability.

Since the  $CP$  varies as a function of length,  $A_{out}$  must be calculated for each position used. Using (2),

$$A_{out} = \sum_{i=1}^{1000} (e^{-\alpha z_{i-1}} - e^{-\alpha z_i}) \quad (7)$$

where  $i$  is the bin number,  $z_i = (z - w_{depl})(i)(\Delta z_i)$ ,  $\Delta z_i = 0.001$ , and  $w_{depl}$  is the depletion region width. The absorption in each bin is subtracted from the previous one so the value isn't cumulative. The EQE is then found, with the results displayed in Table 1.

The ratio of the number of carriers readout to the number of photons absorbed, or the internal quantum efficiency (IQE), can also be compared between the materials. For  $Ge_{0.89}Sn_{0.11}$ , the IQE is simply given by the collection probability  $CP$ , while the IQE for  $In_{0.1}Ga_{0.9}Sb$  is  $CP \times T$  due to the potential barrier.

The refractive index is subsequently calculated, and the minimum interdigitated structure width is found using Snell's law, with the results shown in Table 1.

## Dark Current Estimation

The dark current is critical, especially in low-light conditions in which the photo signal may become overwhelmed by the noise. It is first calculated at 300 K and using 100 mV reverse bias. Only the variables and equations for electrons will be stated to avoid redundancy, with the equivalent expression for holes left to the reader.

First the electron concentration  $n$  of the IR absorber is determined from 2.16a in [20]:

$$n = N_C e^{(E_F - E_c)/kT} \quad (8)$$

The change in electron concentration with respect to time,  $\frac{\partial n}{\partial t}$ , at the IR absorber/Si interface is given by (5)

in [12] and is the dark current in this device

$$\frac{\partial n}{\partial t} = \frac{1}{q} \frac{d}{dx} J_n - R_{bb} - R_{nt} \quad (9)$$

where  $J_n$  is the electron current density,  $R_{bb}$  is the band-to-band recombination term, and  $R_{nt}$  is the transition rate from traps to the conduction band. From (8) in [12]:

$$J_n = qn\mu_n E + qD_n \nabla n \quad (10)$$

where  $\mu_n$  is the electron mobility,  $E$  is the electric field, and  $D_n$  is the diffusion constant in an n-type semiconductor.

The band-to-band recombination coefficient is

$$R_{bb} = B_r (np - n_i^2) \quad (11)$$

where  $B_r$  is the recombination coefficient [13], [14] (19), (Appendix C). The transition rate from traps to the conduction band,  $R_{nt}$ , from [13] (8, 10), is

$$R_{nt}(x) = \int_{E_v}^{E_c} r_{nt}(E_t, x) dE_t \quad (12)$$

$E_t$  is the trap energy and  $r_{nt}$  is the transition rate from traps at particular energy levels to the conduction band [13] (9, 11)

$$r_{nt}(E_t) = c_n n (1 - f_t) D_t - e_n f_t D_t \quad (13)$$

where  $c_n$  is the electron capture coefficient,  $f_t$  is the occupation function,  $D_t$  is the defect density of states, and  $e_n$  is the electron thermal emission rate.  $\frac{\partial n}{\partial t}$  is then

found, with the results shown in Table 1.

## Results

As illustrated in Table 1,  $Ge_{0.89}Sn_{0.11}$ 's target wavelength extends farther into the IR regime due to its smaller bandgap. The minimum interdigitated width for the structures isn't a concern because of the IR absorbing layer's thickness.  $In_{0.1}Ga_{0.9}Sb$ 's EQE and IQE are low because the potential barrier in its conduction band

hinders electron transport to the Si.  $\text{Ge}_{0.89}\text{Sn}_{0.11}$  has more dark current because its smaller bandgap allows carriers to jump into the conduction and valence bands with less energy than in materials with larger bandgaps like  $\text{In}_{0.1}\text{Ga}_{0.9}\text{Sb}$ . To achieve  $1 \text{ pA/cm}^2$  dark current with 100 mV reverse bias,  $\text{Ge}_{0.89}\text{Sn}_{0.11}$  and  $\text{In}_{0.1}\text{Ga}_{0.9}\text{Sb}$  would need to be cooled to 155 K and 185 K, respectively, assuming mid-gap defect state excitation at the heterojunction interface dominates the thermal generation process over that of band-to-band processes.

Parameter	$\text{Ge}_{0.89}\text{Sn}_{0.11}$	$\text{In}_{0.1}\text{Ga}_{0.9}\text{Sb}$
IR Absorber Bandgap	0.48 eV	0.63 eV
Wavelength Range	0.4-2.1 $\mu\text{m}$	0.4-2.0 $\mu\text{m}$
IR Absorber Thickness	100 nm	100 nm
External Quantum Efficiency	23%	0.9%
Internal Quantum Efficiency	~100%	~10%
Minimum Interdigitated Width	48 nm	52 nm
Eff. Trap Density incl. heterojunction interface	$1 \times 10^{17}/\text{cm}^3$	$1 \times 10^{16}/\text{cm}^3$
Dark current at 300 K, 100 mV reverse bias	$12.6 \text{ mA/cm}^2$	$0.22 \text{ mA/cm}^2$
Temp. for $1 \text{ pA/cm}^2$ dark current	155 K	185 K
Potential Readout Noise (4T config.)	<5e- rms	<5e- rms

**Table 1.** Simulated parameters of the proposed detector.

## Discussion

From these calculations, a better understanding of the detector is formed. The results indicate that  $\text{Ge}_{0.89}\text{Sn}_{0.11}$  is more promising than  $\text{In}_{0.1}\text{Ga}_{0.9}\text{Sb}$  to allow photoelectron diffusion into the Si.  $\text{Ge}_{0.89}\text{Sn}_{0.11}$  extends the detector's spectral responsivity farther into the IR range with higher QE, but at the cost of higher dark current. Future work includes depositing the  $\text{Ge}_{0.89}\text{Sn}_{0.11}$  on Si-based CMOS image sensors and testing them. If successful, the widespread use of Si-based IR detectors will be closer to being in reach.

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