

FBK roadmap towards the nextgeneration of 3D-integrated **SiPM and SPAD technologies**

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FBK SiPM technologies Typical Applications

The traditional application of SiPMs is the ToF-PET. In addition, thanks to the constant improvement of SiPM performance, they are being evaluated in the upgrade of several Big Physics Experiments.

Positron Emission Tomography



Big Physics Experiments





Examples of Big Physics experiments FBK is currently working on.

FBK SiPM technologies Use in Big Physics Experiments

Especially for Big Physics Experiments, deep customization of the detector is often required. Strong argument in favor of a <u>custom sensing layer in a 2.5D / 3D integration configuration</u>, <u>optimized for</u> specific applications.

Cryogenic TPCs



Cryogenic SiPMs will be employed in experiments

such as DarkSide-20k





Prototype pSCT installed in the VERITAS, equipped with FBK SiPMs.





NUV-HD SiPMs are being evaluated for the MIP timing detector of CMS (LYSO scintillator readout).





Fondazione Bruno Kessler Custom SiPM technology roadmap



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LG-SiPMs

position-sensitivity



Very small cell pitch

NIR-HD

NIR-UHD

NIR-HD-BSI

Novel structures for NIR detection

2.5D and 3D integration



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2.5D and 3D Integration **FBK IPCEI clean-room upgrade**

FBK is part of the *IPCEI on microelectronics* project (Important Project of Common European Interest - €1.75) billion total public support, 12 M€ to FBK).

The goal for FBK is upgrading its optical sensors technologies, by *developing TSVs, micro-TSV and Backside Illuminated SiPMs*. This will allow high-density interconnections to the front-end and high-segmentation.







2.5D and 3D Integration TSV – via mid: process flow

In the via-mid process, the TSV is formed during the fabrication of the SiPM, modifying its process flow. In the via, the conductor is the highly-doped silicon bulk.

SiPM fabrication + TSV formation



Edge Trimming + BONDING



THINNING



- **NO-DEBONDING** DEBONDING ٠ Thickness at least 150 um Thickness 10-50 um **Glass-less TSV** Standard TSV concept microTSV 500 um SiPM pitch < 50 um SPAD pitch
- Contacts formation

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Metal-free TSV

- Flexible TSV layout and size
- Low bulk resistivity

2.5D and 3D Integration TSV – via mid: first results

Preliminary results on TSV via-mid development, with partial SiPM process, to check isolation and continuity (no Geiger-mode multiplication).





At **-100 V** of bias applied the intensity varies from 30 to 200 fA

Trough Silicon Vias – Via Mid are isolated from the bulk silicon contact



2.5D and 3D Integration 2.5D integrated SiPM tile

In the short and medium term, medium density interconnection seems the sweet spot to obtain excellent performance (e.g. timing) on large photosensitive areas while not increasing complexity and cost too much.

We propose a Photon Detection Module (PDM) in which SiPMs with TSVs down to 1 mm pitch are connected to the readout ASIC on the opposite side of a passive interposer, in a 2.5D integration scheme.



1 - 3 mm interconnection pitch

Hybrid SiPM module being developed for ultimate timing performance in ToF-PET

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Jožef Stefan Institute



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2.5D and 3D Integration 2.5D integrated SiPM tile for timing

The 2.5D integrated PDM (50x50 mm²) will be the basis of a 30x30 cm² ToF-PET panel, which will be used to build limited-angle ToF-PET systems, for brain PET, Cardiac PET and full-body scanners.

We expect very good timing performance, supported by preliminary measurements achieved with NUV-HD SiPMs coupled to FastIC ASIC.



Application of the PDM to build large panes used in new, limted-angle PET applications: Brain Pet, Cardiac PET, while-body PFT

50 mm

Conceptual drawing of the PDM under development

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SPTR and CRT measured at FBK NUV-HD-SiPMs read by the FastIC ASIC developed by ICCUB. **Sensor:** NUV-HD-LFv2 SiPMs, 3x3 mm² Scintillator: 2x2x3 mm³ LSO:Ce,Ca

Power consumption: 3 mW / channel

Single-SPAD TSV **Cross-section**

Exploiting the Deep Trench Isolation, which is anyway present between adjacent SPADs in most SiPMs, we can achieve single SPAD isolation if we thin the wafer down sufficiently (use of a glass support wafer is needed). We can exploit this isolation to build a "bulk" TSV just below and coincident with each single SPAD. The resistors are still on the front-side (no change in signal shape is expected). *Common connection for bias is on the front* and requires a TSV to bring it from the bottom.



Optional metal redistribution layer (might as well be in the ASIC)

Single-SPAD TSV Advantages / Drawbacks

Advantages:

- (Almost) no changes to the state-of-the-art, FSI, NUV-sensitive SiPMs \rightarrow conservative approach.
- It might be the only wat to have fine-pitch TSVs (around 50 um) with <u>no loss of FF</u> for the SiPMs.
- No additional trenches needed \rightarrow simpler.
- Flexibility to have single-cell access, when needed, but also miniSiPMs and microSiPMs, through either a redistribution layer on the SiPM backside or directly in the 3D integrated ASIC
- Connection for the topside metal can be obtained through the same type of vias (possibly with epitaxial layer removal).





2.5D and 3D Integration **High-density integration: DIGILOG**

DIGILOG investigates higher density interconnections to approach the dSiPM performance without the complexity of single-SPAD access.

Single-SPAD TSV will be investigated in the DIGILOG project, removing the need to replace the central SPAD in the uSiPMs with a TSV, thus achieving the *highest PDE possible*.



S. Gundacker, et al., A. Gola, E. Charbon, V. Schultz NSS 2023 S. Gundacker, et al., to be published 2023





μTSV 1/9 of area

- µSiPMs with **µTSVs**
- µASICs with in situ TDCs
- Embedded ANNs

<u>Distributed</u> computing

2.5D and 3D Integration Full 3D integration with micro TSVs: Hybrid SiPM

FBK will also employ the single-SPAD TSV to achieve *single cell connection*. While complexity of the system increases, it might provide *ultimate timing performance*.



- •
- concept.



2.5D and 3D Integration **Backside Illuminated NIR-SiPMs: process flow**

BSI development started on NIR-sensitive SiPMs \rightarrow no need to create a new entrance window on the backside with high efficiency in the NUV.





15

2.5D and 3D Integration BSI NIR SiPMs: first results

The *first NIR-sensitive BSI wafers were fabricated* in FBK clean room (1x1 mm² devices).

Minor differences in the IVs after thinning, compared to the FSI devices (without thinning).

Ultrathin substrate (~ 10 um)





on microelection

NIR PDE comparison at 905 nm



2.5D and 3D Integration **Next-generation development: Backside Illuminated SiPMs**

The next-generation of developments, currently being investigated at FBK, is building a *backside-illuminated*, *NUV-sensitive SiPM*. Several technological challenges should be overcome.

Clear separation between charge collection and multiplication regions.

Potential Advantages:

- <u>Up to 100% FF</u> even with small cell pitch
- Ultimate Interconnection density: < 15 um
- High speed and dynamic range
- Low gain and external crosstalk
- (Uniform) entrance window on the backside, ideal for enhanced optical stack (VUV sensitivity, nanophotonics)
- Local electronics: ultra fast and possibly low-power.



Radiation hardness:

- The SiPM area sensitive to radiation damage, is much smaller than the light sensitive area
- **Assumption**: the main source of DCR is field-enhanced generation (or tunneling).





Collection Region

Development Risks:

- Charge collection time jitter
- Low Gain \rightarrow SPTR?
- Effectiveness of the new entrance window

New BSI-SiPM structure

- technology at FBK:
- Fabio Acerbi
- Ibrahim Mohamed Ahmed
- Lorenzo Barsotti
- Andrea Ficorella
- Priyanka Kachru
- Oscar Marti Villareal
- Stefano Merzi
- Elena Moretti
- Giovanni Palù
- Laura Parellada Monreal
- Giovanni Paternoster
- Michele Penna
- Maria Ruzzarin
- Gianluca Vedovelli
- Nicola Zorzi

Thank you!





Thanks to all the members of the team working on custom SiPM

Customized sensing layers



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Reduction of optical crosstalk NUV-HD-MT development

Starting from the NUV-HD technology, FBK and Broadcom jointly developed the NUV-HD-MT technology, adding metal-filled DTI isolation to strongly suppress optical crosstalk.

Other changes: low electric field variant, layout optimized for timing.





Merzi, Stefano, et al. "NUV-HD SiPMs with metal-filled trenches." Journal of Instrumentation 18.05 (2023): P05040.

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PDE vs. wavelength measured on the NUV-HD-MT technology with 45 um cell size with different values of the excess bias.



Timing performance Improvement of SPTR

Single Photon Time Resolution (SPTR) was improved with a layout that enhances signal extraction and signal integrity, in combination with *small SiPM active area* and a high frequency readout.

With *larger SiPMs*, the SPTR can be preserved by segmenting the active area into smaller pixels, or miniSiPMs, with separate 3D connection to readout, followed by suitable combination of time pick-off information.



Medicine & Biology 64.5 (2019): 055012.

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Radiation Hardness of SiPMs Single SPAD switch-off to reduce the DCR after irradiation

Whether switching off "screamer" SPAD is effective to reduce DCR after irradiation depends on whether the increase of DCR is caused by:

- few, very rare, very "bad" bulk damage events, each one causing a large increase of the DCR \rightarrow single SPAD switch-off is useful.
- b. the sum of many, uniformly distributed, smaller events, each one responsible for smaller DCR increments \rightarrow single SPAD switch-off is not very useful.





DCR vs Fluence for different FBK technologies: all plots converge to similar values above approximately 1e8 n_{eq}/cm^2

Emission microscopy measurement on a NUV-HD SiPM irradiated at 1.10¹¹ n_{eq}/cm², at 4V excess bias, showing almost uniform cell activation.

Altamura, Anna Rita, et al. "Radiation damage on SiPMs for space applications." NIM-A 1045 (2023): 167488.

Acerbi, F., et al. "Characterization of radiation damages on Silicon photomultipliers by X-rays up to 100 kGy." NIM-A 1045 (2023): 167502.

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Reports on non-uniform SPAD DCR after irradiation presented at NSS2023 by L. Ratti