

## **FBK roadmap towards the nextgeneration of 3D-integrated SiPM and SPAD technologies**

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The traditional application of SiPMs is the ToF-PET. In addition, thanks to the *constant improvement of SiPM performance*, they are being evaluated in the *upgrade of several Big Physics Experiments*.

Positron Emission Tomography | The Rig Physics Experiments

## **Typical Applications FBK SiPM technologies**





Examples of Big Physics experiments FBK is currently working on.





Especially for Big Physics Experiments, *deep customization of the detector is often required.* Strong argument in favor of a *custom sensing layer in a 2.5D / 3D integration configuration, optimized for specific applications.*

## **Use in Big Physics Experiments FBK SiPM technologies**



Cryogenic SiPMs will be employed in experiments

such as DarkSide-20k

Cryogenic TPCs

Prototype pSCT installed in the VERITAS, equipped with FBK SiPMs.



NUV-HD SiPMs are being evaluated for the MIP timing detector of CMS (LYSO scintillator readout).







Novel structures for NIR detection

### **Custom SiPM technology roadmap Fondazione Bruno Kessler**



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### **LG-SiPMs**

position-sensitivity



### **NIR-HD**

Very small cell pitch

### **NIR-UHD**

### **NIR-HD-BSI**

# **2.5D and 3D integration**



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FBK is part of the *IPCEI on microelectronics* project (Important Project of Common European Interest - €1.75 billion total public support, 12 M€ to FBK).

The goal for FBK is upgrading its optical sensors technologies, by *developing TSVs, micro-TSV and Backside Illuminated SiPMs*. This will allow high-density interconnections to the front-end and high-segmentation.

### **FBK IPCEI clean-room upgrade 2.5D and 3D Integration**





In the via-mid process, the *TSV is formed during the fabrication of the SiPM, modifying its process flow*. In the via, the *conductor is the highly-doped silicon bulk*.

## **TSV – via mid: process flow 2.5D and 3D Integration**

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• SiPM fabrication + TSV formation



- **Metal-free TSV**
- Flexible TSV layout and size
- Low bulk resistivity

• Edge Trimming + BONDING



**THINNING** 



- DEBONDING Thickness at least 150 um **Glass-less TSV** concept 500 um SiPM pitch • NO-DEBONDING Thickness 10-50 um Standard TSV **microTSV** < 50 um SPAD pitch
- Contacts formation



Preliminary results on TSV via-mid development, with partial SiPM process, to *check isolation and continuity*  (no Geiger-mode multiplication).

### **TSV – via mid: first results 2.5D and 3D Integration**

At **-100 V** of bias applied the intensity varies from **30 to 200 fA**





Trough Silicon Vias – Via Mid are isolated from the bulk silicon contact

In the *short and medium term*, medium density interconnection seems the sweet spot to obtain *excellent performance (e.g. timing) on large photosensitive areas while not increasing complexity and cost too much*.

We propose a Photon Detection Module (PDM) in which *SiPMs with TSVs down to 1 mm pitch* are connected to the *readout ASIC on the opposite side of a passive interposer*, in a *2.5D integration scheme*.

### **2.5D integrated SiPM tile 2.5D and 3D Integration**

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Jožef Stefan Institute



**MASSACHUSETTS GENERAL HOSPITAL** 



Hybrid SiPM module being developed for ultimate timing performance in ToF-PET

### 1 - 3 mm interconnection pitch



The 2.5D integrated PDM (50x50 mm<sup>2</sup> ) will be the basis of a *30x30 cm<sup>2</sup> ToF-PET panel*, which will be used to build limited-angle ToF-PET systems, for brain PET, Cardiac PET and full-body scanners.

Application of the PDM to build large panes used in new, limted-angle PET applications: Brain Pet, Cardiac PET, while-body **PFT** 

We *expect very good timing performance*, supported by preliminary measurements achieved with NUV-HD SiPMs coupled to FastIC ASIC.



### **2.5D integrated SiPM tile for timing 2.5D and 3D Integration**

**10** SPTR and CRT measured at FBK NUV-HD-SiPMs read by the FastIC ASIC developed by ICCUB. **Sensor:** NUV-HD-LFv2 SiPMs, 3x3 mm<sup>2</sup> **Scintillator:** 2x2x3 mm<sup>3</sup> LSO:Ce,Ca **Power consumption**: 3 mW / channel

Conceptual drawing of the PDM under development

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50 mm

*Exploiting the Deep Trench Isolation*, which is anyway present between adjacent SPADs in most SiPMs, we can achieve single SPAD isolation if we thin the wafer down sufficiently (use of a glass support wafer is needed). We can exploit this isolation to *build a "bulk" TSV just below and coincident with each single SPAD*. The *resistors* are still on the front-side (no change in signal shape is expected). *Common connection for bias is on the front* and requires a TSV to bring it from the bottom.

### **Cross-section Single-SPAD TSV**



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Vbias Optional metal redistribution layer (might as well be in the ASIC)



Advantages:

- (Almost) *no changes to the state-of-the-art, FSI, NUV-sensitive SiPMs*  $\rightarrow$  conservative approach.
- It might be the only wat to have fine-pitch TSVs (around 50 um) with *no loss of FF* for the SiPMs.
- No additional trenches needed  $\rightarrow$  simpler.
- Flexibility to have single-cell access, when needed, but also miniSiPMs and *microSiPMs*, through *either a redistribution layer* on the SiPM backside or directly in the *3D integrated ASIC*
- *Connection for the topside metal* can be obtained through the same type of vias (possibly with epitaxial layer removal).



### **Advantages / Drawbacks Single-SPAD TSV**

DIGILOG investigates higher density interconnections to *approach the dSiPM performance without the complexity of single-SPAD access*.

*Single-SPAD TSV will be investigated* in the DIGILOG project, removing the need to replace the central SPAD in the uSiPMs with a TSV, thus achieving the *highest PDE possible*.



## **High-density integration: DIGILOG 2.5D and 3D Integration**



- µSiPMs with µTSVs
- µASICs with *in situ* TDCs
- Embedded ANNs

### • **Distributed computing**

S. Gundacker, et al., A. Gola, E. Charbon, V. Schultz *NSS* 2023 S. Gundacker, et al., *to be published* 2023



µTSV 1/9 of area

FBK will also employ the single-SPAD TSV to achieve *single cell connection*. While complexity of the system increases, it might provide *ultimate timing performance*.

## **Full 3D integration with micro TSVs: Hybrid SiPM 2.5D and 3D Integration**



- 
- *concept*.



BSI development started on *NIR-sensitive SiPMs* → *no need to create a new entrance window* on the backside with high efficiency in the NUV.

### **Backside Illuminated NIR-SiPMs: process flow 2.5D and 3D Integration**



![](_page_14_Picture_4.jpeg)

The *first NIR-sensitive BSI wafers were fabricated* in FBK clean room (1x1 mm<sup>2</sup> devices).

Minor differences in the IVs after thinning, compared to the FSI devices (without thinning).

Ultrathin substrate  $($  ~ 10 um $)$ NIR BSI process is working!  $E-12$  $1.0E - 10 -$ After Thinning  $1.0E - 11 1.0E - 12 -$ Before Thinning  $1.0E - 13 1.0E - 14$ 

 $0.0$ 

 $-1.0$ 

 $-2.0$ 

 $1.0$ 

 $1.5E - 15 -$ 

 $-4.0$ 

 $-3.0$ 

## **BSI NIR SiPMs: first results 2.5D and 3D Integration**

![](_page_15_Figure_4.jpeg)

![](_page_15_Figure_5.jpeg)

![](_page_15_Picture_7.jpeg)

The next-generation of developments, currently being investigated at FBK, is building a *backside-illuminated, NUV-sensitive SiPM*. Several technological challenges should be overcome.

Clear *separation between charge collection and multiplication regions*.

## **Next-generation development: Backside Illuminated SiPMs 2.5D and 3D Integration**

![](_page_16_Picture_15.jpeg)

- Up to 100% FF even with small cell pitch
- Ultimate Interconnection density: < 15 um
- High speed and dynamic range
- Low gain and external crosstalk
- (Uniform) entrance window on the backside, ideal for enhanced optical stack (VUV sensitivity, nanophotonics)
- **Local electronics: ultra fast and possibly** low-power.

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### *Potential Advantages:*

![](_page_16_Figure_10.jpeg)

### *Radiation hardness:*

- The SiPM area sensitive to radiation damage, is much smaller than the light sensitive area
- **Assumption**: the main source of DCR is field-enhanced generation (or tunneling).

Thanks to all the members of the team working on custom SiPM

- technology at FBK:
- **Fabio Acerbi**
- **Ibrahim Mohamed Ahmed**
- **Lorenzo Barsotti**
- **Andrea Ficorella**
- **Priyanka Kachru**
- **Oscar Marti Villareal**
- **Stefano Merzi**
- **Elena Moretti**
- **Giovanni Palù**
- **Laura Parellada Monreal**
- **Giovanni Paternoster**
- **Michele Penna**
- **Maria Ruzzarin**
- **Gianluca Vedovelli**
- **Nicola Zorzi**

# **Thank you!**

![](_page_17_Picture_17.jpeg)

![](_page_17_Picture_18.jpeg)

# **Customized sensing layers**

![](_page_18_Picture_1.jpeg)

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![](_page_18_Picture_3.jpeg)

![](_page_19_Picture_12.jpeg)

Starting from the NUV-HD technology, FBK and Broadcom jointly developed the NUV-HD-MT technology, adding *metal-filled DTI isolation to strongly suppress optical crosstalk*.

Other changes: low electric field variant, layout optimized for timing.

## **NUV-HD-MT development Reduction of optical crosstalk**

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## **J. BROADCOM®**

Merzi, Stefano, et al. "NUV-HD SiPMs with meta trenches." *Journal of Instrumentation* 18.05 (2023): P05040.

![](_page_19_Figure_8.jpeg)

![](_page_19_Figure_3.jpeg)

![](_page_19_Picture_4.jpeg)

PDE vs. wavelength measured on the NUV-HD-MT technology with 45 um cell size with different values of the excess bias.

*Single Photon Time Resolution (SPTR)* was improved with a *layout that enhances signal extraction* and signal integrity, in combination with *small SiPM active area* and a high frequency readout.

With *larger SiPMs*, the SPTR can be preserved by *segmenting the active area into smaller pixels, or miniSiPMs*, with *separate 3D connection to readout*, followed by *suitable combination of time pick-off* information.

![](_page_20_Figure_3.jpeg)

### **Improvement of SPTR Timing performance**

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![](_page_20_Picture_4.jpeg)

*Whether switching off "screamer" SPAD is effective* to reduce DCR after irradiation depends on whether the increase of DCR is caused by:

- *few, very rare, very "bad" bulk damage events, each one causing a large increase of the DCR → single SPAD* switch-off is useful.
- *b. the sum of many, uniformly distributed, smaller events*, each one responsible for smaller DCR increments → single SPAD switch-off is not very useful.

SiPM irradiated at 1·10 $^{11}$  n<sub>eq</sub>/cm<sup>2</sup>, at 4V excess bias, showing *almost uniform cell activation*.

DCR vs Fluence for different FBK technologies: all plots converge to similar values above approximately 1e8  $n_{eq}/cm^2$ 

## **Single SPAD switch-off to reduce the DCR after irradiation Radiation Hardness of SiPMs**

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Emission microscopy measurement on a NUV-HD

![](_page_21_Figure_4.jpeg)

![](_page_21_Picture_5.jpeg)

Reports on non-uniform SPAD DCR after irradiation presented at NSS2023 by L. Ratti

*Acerbi, F., et al. "Characterization of radiation damages on Silicon photomultipliers by X-rays up to 100 kGy." NIM-A 1045 (2023): 167502.*

*Altamura, Anna Rita, et al. "Radiation damage on SiPMs for space applications." NIM-A 1045 (2023): 167488.*