

2021 INTERNATIONAL IMAGE SENSOR WORKSHOP

Online event

Sept 20-23, 2021

Paper publication date: Sept 13th, 2021

PROGRAM

Monday, Sept 20th 2021

15:00 Opening

Session 01 Small Pixels

Session chair: TBA

- 15:12
R01 The-State-of-the-Art of CMOS Image Sensors
Ziad Shukri, TechInsights Inc., Ottawa, Canada
- 15:18
R02 A 40/22nm 200MP Stacked CMOS Image Sensor with 0.61um Pixel
Masayuki Uchiyama², Geunsook Park¹, Tomoyasu Tate¹, Masashi Minagawa², Shino Shimoyamada², Zhiqiang Lin¹, King Yeung¹, Lien Tu¹, Wu-Zang Yang³, Alan Hsiung¹, Vincent Venezia¹, Lindsay Grant¹
¹ *OmniVision Technologies, Santa Clara, CA, USA*
² *OmniVision Technologies Japan, Kanagawa, Japan*
³ *OmniVision Technologies Taiwan, Hsinchu, Taiwan*
- 15:24
R03 0.64µm-pitch CMOS Image Sensor with Low Leakage Current of Vertical Transfer Gate
Dongmo Im, Jameyung Kim, Juhee Lee, Sungbong Park, Kwansik Cho, Hyunchul Kim, Chang-Rok Moon and Hyoung-Sub Kim
Semiconductor R&D Center, Samsung Electronics Co., Republic of Korea.
- 15:30
R04 An application of sub-half-micron pixel with preserved color pixel concept for next-generation color image acquisition
Yuichiro Yamashita¹, Yuichiro Yamashita¹, Rihito Kuroda^{2,3}, Shigetoshi Sugawa³, Dun Nian Young¹
¹ *CMOS Image Sensor Division, Taiwan Semiconductor Manufacturing Company, Hsinchu, Taiwan, R.O.C.*
² *Graduate School of Engineering, Tohoku University, Sendai, Japan,*
³ *New Industry Creation Hatchery Center, Tohoku University, Sendai, Japan*
- 15:36
R05 A study on modulation transfer function and signal-to-noise ratio for Tetracell CMOS image sensors with sub-micrometer scale unit pixels
Jae Ho Kim, Euiyoung Song, Kwanghee Lee, Bumsuk Kim, DaeKwan Kim, Uihui Kwon, Dae Sin Kim
Samsung Electronics, Hwaseong, Korea,
- 15:42
R06 Performance Analysis of Two Low-Noise 0.8um Pixel Designs
Leo Anzaigra, Jiaju Ma, Donald Hondongwa, Dexue Zhang, Saleh Masoodian
Gigajot Technology Inc, 3452 E Foothill Blvd, Suite 360, Pasadena , CA 91107
- 15:48
R07 Novel non metallic pixel isolation technology for high sensitivity in CMOS image sensors with submicron pixels
Hye Yeon Park¹, Yunki Lee¹, In-Sung Joe², Boseong Kim¹, Taehan Kim¹, Jeongmin Bae¹, Hyunseok Song¹, Kwangmin Lee², Minsung Heo², Jinhyung Kim², Jaehak Lee³, Jungho Park³, Hyunchul Kim², Chang-Rok Moon², Bumsuk Kim¹, JungChak Ahn¹, Duckhyun Chang¹
¹ *System LSI Division, Samsung Electronics Co., Ltd*
² *Samsung Electronics, Semiconductor R&D Center*
³ *Foundry Division, Samsung Electronics Co., Ltd, Korea,*
- 15:54 Break**

Session 02 Low Noise and Photon Counting

Session chair: TBA

- 16:00
R08 Time Domain Noise Analysis of Oversampled CMOS Image Sensors
Boyd Fowler, Andreas Suess, Mathias Wilhelmsen and Liang Zuo
OmniVision Technologies, Santa Clara, CA, USA
- 16:06
R09 Multi-Gate Source-Follower for Quanta Image Sensors (QIS)
Wei Deng and Eric R. Fossum
Thayer School of Engineering, Dartmouth College, NH, USA
- 16:12
R10 Characterization of Random Telegraph Noises of MOSFET Subthreshold Currents for a 40nm Process
Calvin Yi-Ping Chao, Meng-Hsu Wu, Shang-Fu Yeh, Chi-Lin Lee, Yin Chin, Kuo-Yu Chou, and Honyih Tu
Taiwan Semiconductor Manufacturing Company, Hsinchu, Taiwan, ROC
- 16:18
R11 A 124dB Dynamic-Range SPAD Photon Counting Image Sensor Using Subframe Extrapolation
Jun Ogi, Takafumi Takatsuka, Kazuki Hizu, Yutaka Inaoka, Hongbo Zhu, Yasuhisa Tochigi, Yoshiaki Tashiro, Fumiaki Sano, Yusuke Murakawa†, Makoto Nakamura†, Yusuke Oike
Sony Semiconductor Solutions, †Sony Semiconductor Manufacturing
- 16:24
R12 Quanta Burst Photography
Sizhuo Ma¹, Shantanu Gupta¹, Arin C. Ulku², Claudio Bruschini², Edoardo Charbon², Mohit Gupta¹
¹ University of Wisconsin-Madison, USA
² EPFL, Switzerland
- 16:30
R13 A 1-Transistor SPAD Quanta Image Sensor for High-Speed and Small-Pitch Arrays
M. Perenzoni, L. Parmesan, F. Acerbi
Fondazione Bruno Kessler – Sensors and Devices, Trento, Italy
- 16:36
R14 A Simple Monte Carlo Transport and Multiplication Simulation Method for the Analysis of a SPAD with a Spherically Uniform Electric Field Peak
Edward Van Sieleghem^{1,2}, Andreas Süss^{2,3}, Gauri Karve², Koen De Munck², Chris Van Hoof^{1,2}, Jiwon Lee²
¹ KU Leuven, ESAT, Kasteelpark Arenberg 10, 3001 Leuven, Belgium
² Imec, Kapeldreef 75, 3001 Leuven, Belgium
³ now at OmniVision Technologies, Santa Clara CA 95054, USA
- 16:42 Break**

Session 03 Time-of-Flight

Session chair: TBA

- 16:48
R15 A Reconfigurable QVGA/Q3VGA Direct Time-of-Flight 3D Imaging System with On-chip Depth-map Computation in 45/40nm 3D-stacked BSI SPAD CMOS
David Stoppa¹, Sargis Abovyan², Daniel Furrer¹, Radoslaw Gancarz¹, Thomas Jessenig², Robert Kappel², Manfred Lueger², Christian Mautner², Ian Mills¹, Daniele Perenzoni¹, Georg Roehrer², Pierre-Yves Taloud¹
¹ ams International AG, Eggstrasse 91, 8803 Rueschlikon, Switzerland
² ams AG, Tobelbader Strasse 30, 8141 Premstaetten, Austria

- 16:54
R16 Demodulation Contrast simulation for indirect Time-Of-Flight sensors based on Fast Photo-Diode
G. Mugny[§], P. Fonteneau*[§], B. Rodrigues-Goncalves†, J.M. Melo Santos‡, M. Vignetti†, C. Tubert*, A. Crocherie†, D. Rideau†, F. Lalanne†, V. Farys† and A. Tournier†*
* STMicroelectronics, Grenoble, France
† STMicroelectronics, Crolles, France
‡ STMicroelectronics, Edinburgh, Scotland, UK
- 17:00
R17 A 3-Tap Global Shutter 5.0um Pixel with Background Canceling for 165MHz Modulated Pulsed Indirect Time-of-Flight Image Sensor
Masafumi Tsutsui¹, Toshifumi Yokoyama¹, Takahisa Ogawa¹, Ikuo Mizuno¹, Nobuyoshi Takahashi¹, C. Ma², F. Wang³, J. Debondt⁴, J. Bogaerts⁴, X. Wang² & Assaf Lahav⁵
¹ Tower Partners Semiconductor Co. Ltd., Toyama, Japan
² Gpixel INC, Changchun, China
³ Orbbec Shenzhen, Shenzhen, China
⁴ Gpixel NV, Antwerp, Belgium
⁵ Tower Semiconductor, Migdal Haemek, Israel
- 17:06
R18 A 4-tap Lock-in Pixel Time-of-Flight Range Imager with Substrate Biasing and Double-Delta Correlated Multiple Sampling
Keita Yasutomi, Michihiro Inoue, Shohei Daikoku, Mars Kamel, Shoji Kawahito
Research Institute of Electronics, Shizuoka University, Hamamatsu, 432-8011, Japan
- 17:12
R19 CMOS 3D-Stacked FSI Multi-Channel Digital SiPM for Time-of-Flight Vision Applications
Francesco Gramuglia¹, Andrada Muntean¹, Esteban Venialgo¹, Myung-Jae Lee¹, Scott Lindner¹, Makoto Motoyoshi², Andrei Ardelean¹, Claudio Bruschini¹, Edoardo Charbon¹
¹ EPFL, Neuchâtel, Switzerland
² T-Micro, Sendai, Japan
- 17:18
R20 An Ultra-low current operating 5-um Vertical Field Modulator Pixel for in-direct Time of Flight 3D Sensor
Jaehyung Jang¹, Hoonmoo Choi¹, Hyungjune Yoon¹, Jongchae Kim¹, Jongeun Kim¹, Dongjin Lee¹, Jaewon Lee¹, Ohjun Kwon¹, Jungen Song¹, Minseok Shin¹, Kangbong Seo¹
¹ SK hynix, Future Innovation Technology Group, Icheon-si, Gyeonggi-do, Korea
- 17:24
R21 Wiggling error self-calibration for indirect ToF image sensors
Min-Sun Keel, Daeyun Kim, Jiheon Park, Soho Son, Bumsik Chung, Jonghan Ahn, Yeomyung Kim, Myunghan Bae, Hoyong Lee, Myoungoh Ki, Myeonggyun Kye, Il-Pyeong Hwang, Seung-Chul Shin, Young-Gu Jin, Youngsun Oh, Yitae Kim, Jesuk Lee and Duckhyun Chang
Samsung Electronics, Hwaseong, Korea
- 17:30 Networking Lounge**

Tuesday, Sept 21st 2021

15:00 Opening remarks and announcements

Session 04 Circuit Design of Pixels and Readout Circuits

Session chair: TBA

15:06 A Partial-Multi-Conversion Single-Slope ADC with Response-Linearized RDAC
R22 *Akira Matsuzawa, Abdel Martinez Alonso, Lilan Yu, Minhyuk Sung, Masaya Miyahara
Tech Idea Co., Ltd., Tamaku, Kawasaki, 214-0021, Japan*

15:12 A 200kFPS, 256×128 SPAD dToF sensor with peak tracking and smart readout
R23 *Istvan Gyongy¹, Ahmet T. Erdogan¹, Neale A.W. Dutton², Hanning Mai¹, Francesco
Mattioli Della Rocca¹, Robert K. Henderson¹
¹The University of Edinburgh, Institute for Integrated Micro and Nano Systems,
Edinburgh, U.K., ²Imaging Division, STMicroelectronics, Edinburgh, U.K.*

15:18 A 13'000 FPS Vision System-on-Chip with Mixed-Signal Compressed Sensing
R24 *Jens Döge, Christoph Hoppe, Peter Reichel, Nico Peter, Andreas Reichel and Christian
Skubich
Fraunhofer Institute for Integrated Circuits IIS / EAS, Dresden, Germany*

15:24 Unraveling the paradox of intensity-dependent DVS pixel noise
R25 *Rui Graca, Tobi Delbruck
Sensors Group, Inst. of Neuroinformatics, UZH-ETH Zurich, Zurich, Switzerland*

15:30 Energy Harvesting 3D Stacked Image Sensor with Integrated Data Compression
R26 *Filip Kaklin^{1,2}, Jeffrey M. Raynor², Robert K. Henderson¹
¹School of Engineering, Institute for Integrated Micro and Nano Systems, University of
Edinburgh, Edinburgh, UK,
²STMicroelectronics Imaging Division, Edinburgh, UK*

15:36 Invited Presentation I

I1 *Sensor Design Parameters Affecting Automotive Machine Vision
Gabriel Bowers, Mobileye, Israel*

Session 05 Flash Paper Breakout Rooms - session 1

16:00 – 17:30

P01 *Continuous Triple Log Gaussian Dark Current in 3-Tap Indirect ToF Sensors
Takahiro Akutsu, Masanori Nagase and Takashi Watanabe
Brookman Technology, Inc., Hamamatsu, Japan*

P02 *Dark Current Model for the Time of Low Noise & Photon Counting
Dan McGrath
GOODiX Technology, , Lexington, MA, USA*

P03 *Two-photon absorption in CMOS image sensors
Daan Blommaert¹, Guy Meynants¹, and Stefano Guerrieri²
¹Dept. Electrical Engineering (ESAT), KU Leuven, Belgium
²ams Sensors Belgium, Antwerp, Belgium*

P04 *A 5Mpix 5 um 140 fps MWIR Focal Plane Array and Readout Integrated Circuit at 150K
C.G. Jakobson, N. Ben Ari, W. Freiman, N. Shiloah, G. Zohar, T. Argov, O. Cohen, R.
Dobromislin, R. Talmor, O. Magen, L. Shkedy, B. Milgrom, T. Markovitz, I. Shtrichman
Semiconductor Devices, Haifa, Israel*

P05 *Study on the Characteristics of Strain according to the dark effect in 1.12u Pixel
Hoyoung Kwak¹, Jongeun Kim¹, Seongil Kim², Kangbong Seo¹
¹SK hynix, Future Innovation Technology Group, Icheon-si, Gyeonggi-do, Korea
²SK hynix, AT Group, Cheongju-si, Chungcheongbuk-do, Korea*

- P06 Impact of Kickback Noise of Comparator in Single Slope ADC on Photon Transfer Curve Characterization
Shang-Fu Yeh, Meng-Hsu Wu, Chih-Lin Lee, Chin Yin, Kuo-Yu Chou, Hon-Yih Tu, Calvin Yi-Ping Chao Taiwan Semiconductor Manufacturing Company, Hsinchu, Taiwan
- P07 Low-noise and high-performance 3-D pixel transistor for sub-micron CMOS image sensors applications
*Sung-in Kim, Sungbong Park, Jongeun Park, Hyunchul Kim, Chang-Rok Moon and Hyoung-Sub Kim
Semiconductor R&D Center, Samsung Electronics Co., Republic of Korea*
- P08 Towards a High-Speed Photon-Counting CMOS Quanta Image Sensor (QIS)
*Wei Deng and Eric R. Fossum
Thayer School of Engineering, Dartmouth College, Hanover, NH, USA*
- P09 Modeling, Characterization and Simulation of Dielectric Absorption in Capacitors in Image Sensors
*Manuel Innocent
ON Semiconductor, Mechelen, Belgium*
- P10 Improvement of Fluorine to Photo Response Non-Uniformity and Random Telegraph Signal of Pinned Photodiodes
*Wen-Cheng Yen, Han-Chi Liu, and Sen-Huang Huang
PixArt Imaging Inc., Hsinchu, Taiwan, ROC*
- P11 Plasmonic diffraction for the sensitivity enhancement of silicon image sensor
*Atsushi Ono^{1,2}, Kazuma Hashimoto¹, and Nobukazu Teranishi^{2,3}
1 Graduate School of Integrated Science and Technology, Shizuoka University, Hamamatsu Japan
2 Research Institute of Electronics, Shizuoka University, Hamamatsu, Japan
3 Laboratory of Advanced Science and Technology for Industry, University of Hyogo, Hyogo Japan*
- P12 Indirect-ToF system optimization for sensing range enhancement with patterned light source and optimal binning
*Seung-chul Shin, Myeonggyun Kye, Il-pyeong Hwang, Taemin An, Kyu-Min Kyung, Duhyeon Kwak, Hakbeom Jang, Hogyun Kim, Jaeil An, Sunhwa Lee, Yundong Chang, Seongwon Jo, Junghwan Yoo, Myoungoh Ki, Min-Sun Keel, Yitae Kim, Jesuk Lee and Duckhyun Chang
Samsung Electronics, Hwaseong, Korea*
- P13 A hardware and simulation-based framework for design of SPAD receivers in scanning LiDAR systems
*Sarrah M. Patanwala^{1,2}, Hanning Mai¹, Alistair Gorman¹, Andreas Aßmann², Istvan Gyongy¹, Neale A. W. Dutton², Bruce R. Rae², and Robert K. Henderson¹
1 School of Engineering, Institute for Integrated Micro and Nano Systems, University of Edinburgh, UK, 2STMicroelectronics Imaging Division, Edinburgh, UK*
- P14 Planar microlenses applied to SPAD pixels
*L. Dilhan^{1,2}, J. Vaillant², Q. Abadie², A. Ostrovsky¹, S. Verdet², P. Calka², F. Hemeret², L. Masarotto²
1STMicroelectronics, Crolles; France,
2 Univ. Grenoble Alpes, CEA, LETI, Grenoble, France*
- P15 Innovating CMOS pixel potentials and Full Well Capacity extraction methods from test structures.
*C.Doyen^{1,2}, S.Ricq¹, O.Marcelot², P.Magnan²
1STMicroelectronics, Crolles, France
2 ISAE-SUPAERO, Université de Toulouse, Toulouse, France*

- P16 A 1.3M Pixel 34,700fps Global-Shutter BSI Imager with HDR and Motion Blur Suppression Capability
Gaozhan Cai¹, Arno Van Hoorebeeck¹, Bert Luyssaert¹, Bart Dierickx¹, Canol Gokel¹, Tom Van Uffelen¹, Periklis Stampoglis¹, Gerlinde Ruttens¹, Jun Yamane², Kenji Tajima², Idaku Ishii³
¹ *Caeleste CVBA, Mechelen, Belgium*
² *Photron Limited, Tokyo, Japan*
³ *Hiroshima University, Department of System Cybernetics, Hiroshima, Japan*

Wednesday, Sept 22st 2021

15:00	Opening remarks and announcements
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Session 06	Optical Optimization of Pixels
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	Session chair: TBA
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| 15:06
R27 | A Smart Dual Pixel Technology for Accurate and All-Directional Auto Focus in CMOS Image Sensors
<i>Kyungho Lee, Eun Sub Shim, Junghyung Pyo, Wooseok Choi, Jungbin Yun, Taesub Jung, Kyungduck Lee, Seyoung Kim, Chanhee Lee, Seungki Baek, Junseok Yang, Jongwon Choi, Bumsuk Kim, Chang-Rok Moon, JungChak Ahn, and Duckhyun Chang</i>
<i>Samsung Electronics Co., Ltd., Hwaseong-city, Gyeonggi-do, Korea</i> |
| 15:12
R28 | A Compressed N×N Multi-Pixel Imaging and Cross Phase-Detection AF with N×1RGrB+1×NGb Hetero Multi Pixel Image Sensors
<i>Koichi Fukuda</i>
<i>ICB R&D Center 1, Canon Inc.</i>
<i>Graduate School of Engineering, Tohoku University</i> |
| 15:18
R29 | Image sensor with V-shape deflector structures for sensor edge performance improvement
<i>Shin-Hong Kuo, Ta-Yung Ni, Guang-Yu Huang, Huang-Jen Chen, Hui-Min Yang, Hao-Min Chen, Hao-Wei Liu, Yu-Chi Chang, Ching-Chiang Wu, Ken Wu, Hung-Jen Tsai</i>
<i>VisEra Technologies Company, Hsinchu, Taiwan</i> |
| 15:24
R30 | Quantum Efficiency and Optical Cross-talk of Pixels with Backside Scattering Technique for Near-Infrared Imaging
<i>Tae-Yon Lee, Seungjae Oh, Taehyoung Kim, Hongki Kim, Jihyun Kwak, Masaru Ishii, Surim Lee, Hyoju Kim, Yoonjay Han, Changhwa Kim, Jaehoon Jeon, Dongseok Cho, Seung Sik Kim, Jonghyun Go, In-Gyu Baek, Hyuksoon Choi, Jaekyu Lee, and Chang-Rok Moon</i>
<i>Semiconductor R&D Center, Samsung Electronics Co.,Ltd., Gyeonggi-do, Korea</i> |
| 15:30
R31 | Novel Optical Fingerprint CMOS Image Sensor for Ultra-thin Module
<i>Yunki Lee¹, Jonghoon Park¹, Bumsuk Kim¹, Junsung Park¹, Bomi Kim¹, Taehan Kim¹, Yunji Jung¹, Yunchul Han¹, Seungjae Yoo¹, Sungkwan Kim², Junetaeg Lee², Jesuk Lee¹, Chang-Rok Moon³, Jungchak Ahn¹, and Duckhyun Chang¹</i>
¹ <i>Samsung Electronics, System LSI Business, Yongin-si, Gyeonggi-do, Korea.</i>
² <i>Samsung Electronics, Foundry Business, Yongin-si, Gyeonggi-do, Korea</i>
³ <i>Samsung Electronics, Semiconductor R&D Center, Hwasung-si, Gyeonggi-do, Korea.</i> |
| 15:36
R32 | 1.4mm pixel, 8Mpixel, thick epi image sensor for RGB-IR imaging
<i>Jeongeun Song, Sunyoung Lee, Minseok Shin, Sunguk Seo, Hansang Kim, Jinuk Jeon, Ohjun Kwon, Jaehyung Jang, Kangbong Seo</i>
<i>SK Hynix semiconductor Inc, Ichon city, kyoungki province, 464-8603, Korea</i> |

15:42
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Invited Presentation II

EUV Lithography: driving worldwide innovation using advanced technology
Wim van der Zande, ASML, The Netherlands

Session 07 Flash Paper Breakout Rooms - Session 2

16:00 – 17:30

- P17 A 10-bit 10x10 25µm-Pixel NIR Camera Using Backside-illuminated Ge-on-Si Detectors and Ultra-low-power Direct-injection ROICs
Steffen Epple¹, Zili Yu¹, Mathias Kaschel¹, Michael Oehme², Maurice Warnitzek^{2,1}, Joerg Schulze², Joachim N. Burghartz¹
¹ *Institut für Mikroelektronik Stuttgart (IMS CHIPS), Stuttgart, Germany*
² *Institut für Halbleitertechnik (IHT), Universität Stuttgart, Stuttgart, Germany*
- P18 Development of an Advanced NIR Multispectral Technology Camera System with Potential Industrial and Medical Innovative Applications
Hirofumi Sumi^{1,2}, Hironari Takehara¹, and Jun Ohta¹
¹ *Graduate School of Science and Technology, Nara Institute of Science and Technology (NAIST), Japan*
² *Institute of Industrial Science, The University of Tokyo, Japan*
- P19 SPAD based Time of Flight Pixel Circuits for Large Scale Arrays
Kasper Buckbee^{1,2}, Neale Dutton², Robert K. Henderson¹
¹ *School of Engineering, Institute for Integrated Micro and Nano Systems, University of Edinburgh, Edinburgh, UK*, ² *STMicroelectronics Imaging Division, Edinburgh, UK*
- P20 A Novel Ultra-High-Speed CMOS Image Sensor Implementation with Variable Spatial and Temporal Resolution using Temporal Pixel Multiplexing
Deividas Krukauskas, Ben Marsh, Iain Sedgwick, Nicola Guerrini, Seddik Benhammadi
CMOS Sensor Design Group, Science and Technology Facilities Council (Now part of UK Research and Innovation), Rutherford Appleton Laboratory, Harwell Science and Innovation Campus, Didcot, Oxfordshire, United Kingdom
- P21 Charge Level Control with a Capacitive Trench for Imaging Devices
P.Touron¹, F.Roy¹, P.Magnan², C.Virmontois³ and S.Demiguel⁴
¹ *STMicroelectronics, Crolles, France*
² *ISAE-Supaero, Université de Toulouse, Toulouse, France,*
³ *CNES, Toulouse, France,*
⁴ *Thales Alenia Space, Cannes, France*
- P22 A rad-hard, 60µm pixel sensor optimized for the direct detection of electrons
M. Sannino^a, A. Bofill-Petit^a, M. Giulioni^a, A. Mollà Garcia^a, and R. Turchetta^a, G. McMullan^b and R. Henderson^b, C. Copetti^c, B. Janssen, L. Mele^c and G. van Duinen^c
^a *IMASENIC Advanced Imaging S.L., Barcelona, Spain*
^b *Laboratory of Molecular Biology, Cambridge, UK*
^c *ThermoFisher Scientific, Eindhoven, NL*
- P23 A 1280 x 1024 Backside Illuminated CMOS Image Sensor with 0.75e- noise, 25fps and 180mW Power Consumption
Yang Liu^{1,2}, Tao Jiang¹, Jing Li¹, Cheng Ma¹, Quan Zhou¹, Xinyang Wang¹
¹ *Gpixel INC, Changchun, China*
² *University of Chinese Academy of Sciences, Beijing, China*
- P24 Characterization of Random Telegraph Signal Histogram according to Floating Diffusion Potential in CMOS Image Sensor
Dongho Ha, Hyungjun Han, Eun-Khwang Lee, Wonjun Lee, Dong Guk Lee, Gayoung Keum, Sieuoo Kim, Jiye Hwang, Hoon-Sang Oh, Changrock Song
CIS Development Group, SK Hynix Inc., Icheon-si, Gyeonggi-do, Republic of Korea

- P25 A Multi-Simultaneous-Gate CMOS Lock-in Pixel Image Sensor for Time-Resolved Near-Infrared Spectroscopy
Lioe De Xing, Yuya Shirakawa, Keita Yasutomi, Keiichiro Kagawa, Shoji Kawahito
Research Institute of Electronics, Shizuoka University
- P26 Depth precision in dToF sensors for AR applications
Preethi Padmanabhan, Scott Lindner, Pierre-Yves Taloud, and David Stoppa
ams International AG, Rueschlikon, Switzerland
- P27 A Multi-Junction Photodetector with Dual Four Transistor Structure to Detect Visible and Near-infrared Light in One Single Pixel.
Weihan Hu¹ and Albert J. P. Theuwissen^{1,2}
¹ *Electronic Instrumentation Laboratory, Delft University of Technology, Delft, The Netherlands,* ² *Harvest Imaging, Bree, Belgium*
- P28 On the covariance and variance in the determination of PTC
Peter Centen¹, Jeroen Rotte²
¹ *PeerImaging consulting, Goirle, The Netherlands*
² *Grass Valley, Breda, The Netherlands*
- P29 High-ambient, super-resolution depth imaging with a SPAD imager via frame re-alignment
Germán Mora-Martín¹, Abderrahim Halimi², Robert K. Henderson¹, Jonathan Leach², Istvan Gyongy¹
¹ *The University of Edinburgh, Institute for Integrated Micro and Nano Systems, Edinburgh, U.K.*
² *Heriot-Watt University, Institute of Photonics and Quantum Sciences, Edinburgh, U.K.*
- P30 Dynamic Crosstalk Analysis for Branching Image Sensors
Nguyen H. Ngo¹, Takayoshi Shimura², Taeko Ando¹, Heiji Watanabe², Kazuhiro Shimonomura¹, Yoshinari Kamakura³, Hideki Mutoh⁴, T. Goji Etoh¹
¹ *Ritsumeikan University, Kusatsu, Shiga, Japan,* ² *Osaka University, Osaka, Japan*
³ *Osaka Institute of Technology, Osaka, Japan*
⁴ *Link Research Corporation, Kanagawa, Japan*
- P31 Threshold Uniformity Improvement in 1b QIS Readout Circuit
Zhaoyang Yin^{1,2}, Jiaju Ma², Saleh Masoodian² and Eric R. Fossum¹
¹ *Thayer School of Engineering, Dartmouth College, Hanover, NH, USA*
² *Gigajot Technology, Inc., Pasadena, CA, USA*
- P32 5.6 μm charge domain global shutter pixel with 85dB shutter efficiency
Guy Meynants^{1,2}, Martin Waeny¹, Deyan Levski¹, Rostislav Kandilarov¹, Denis Sami¹, Georgi Bochev¹, Manlyun Ha³, WooSung Choi³, DongJun Oh³, SeongJin Kim³, YongChan Kim³
¹ *Photolithics, Ruse, Bulgaria*
² *Dept. Electrical Engineering (ESAT), KU Leuven, Belgium*
³ *DB Hitek, Korea*

Thursday, Sept 23th 2021

15:00 **Opening remarks and announcements**
Best Flash Presentation Paper Award

15:06 **Walter Kosonocky Award**

Session 08 High-Speed Imagers

Session chair: TBA

15:12 An Extremely High-Speed and Low-Power Digital Pixel Sensor with Advanced Sensor
R33 Architecture
Myunglae Chu, Min-Woong Seo, Suksan Kim, Hyun-Yong Jung, Jiyoun Song, Sung-Jae Byun, Minkyung Kim, Daehee Bae, Junan Lee, Sung-Yong Kim, Jongyeon Lee, Jonghyun Go, Jae-kyu Lee, Changrok Moon, Hyoung-Sub Kim
Samsung Electronics, Hwaseong-si, Republic of Korea

15:18 Toward Super Temporal Resolution by Controlling Horizontal Motions of Electrons
R34 *T. Goji Etoh^{1,2}, Nguyen Hoai Ngo², Kazuhiro Shimonomura², Taeko Ando², Takayoshi Shimura¹, Heiji Watanabe¹, Hideki Mutoh³, Yoshinari Kamakura⁴, Edoardo Charbon⁵*
¹Osaka University, ²Ritsumeikan University, ³Link Research Corp., ⁴Osaka Inst. Tech., ⁵EPFL

15:24 A 1000fps High SNR Voltage-domain Global Shutter CMOS Image Sensor with Two-
R35 stage LOFIC for In-Situ Fluid Concentration Distribution Measurements
Tetsu Oikawa¹, Rihito Kuroda^{1,2}, Keigo Takahashi¹, Yoshinobu Shiba^{2,3}, Yasuyuki Fujihara¹, Hiroya Shike¹, Maasa Murata¹, Chia-Chi Kuo¹, Yhang Ricardo Sipaubo Carvalho da Silva¹, Tetsuya Goto², Tomoyuki Suwa², Tatsuro Morimoto², Yasuyuki Shirai², Masaaki Nagase³, Nobukazu Ikeda³, and Shigetoshi Sugawa²
¹ Graduate School of Engineering, Tohoku University, Sendai, Miyagi, Japan
² New Industry Creation Hatchery Center, Tohoku University, ³ Fujikin Incorporated

15:30 A 40000fps global shutter image sensor with 26.7ns 12-bit row readout time
R36 *A. Xhakoni, A. Fekri, P. Francis, K. Vancauwenbergh, K. Van Esbroeck*
AMS, Antwerp, Belgium

15:36 High speed 21Mpixel global shutter image sensor
R37 *T. Blanchaert¹, B. Ceulemans¹, B. Wolfs¹, W. Cotteleer¹, G. Lepage¹, G. Vanhorebeek¹, E. Markey¹, A. Huysman¹, T. Jiang², Y. Li², C. Ma², J. Bogaerts¹*
¹ Gpixel NV, Antwerp, Belgium, ²Gpixel Inc, Changchun, China

15:42 A 1Mpixel, 80k fps Global Shutter CMOS Image Sensor for High Speed Imaging
R38 *Daniel Van Blerkom, Loc Truong, Jeff Rysinski, Radu Corlan*, Karthik Venkatesan, Sam Bagwell, Jonathan Bergey*
Forza Silicon, Pasadena, California, USA
** Vision Research, Wayne, New Jersey, USA*

15:48 A 2.5um 9.5 Mpixel charge domain global shutter imager with dual columns and 7.1
R39 Gbps per channel outputs for high framerate applications
Jeroen Rotte, Rik Visser, Ruud van Ree, Frank van der Weegen, Klaas Jan Damstra
Grass Valley, Breda, the Netherlands

15:54 **Break**

Session 09 Scientific and Non-Silicon Based Imagers

Session chair: TBA

16:00 Detailed Characterization of SWIR-sensitive Colloidal Quantum Dot Image Sensors
R40 *Joo Hyoung Kim¹, Vladimir Pejovic^{1,2}, Epimtheas Georgitzikis¹, Yunlong Li¹, Pawel E. Malinowski¹, Itai Liebermann¹, David Cheyns¹, Paul Heremans^{1,2}, and Jiwon Lee¹*
¹ imec, Leuven, Belgium, ² KU Leuven, Leuven, Belgium

- 16:06
R41 Crystalline Selenium Layer Stacked CMOS Image Sensors with Pixel-Wise 1-bit A/D Converters using Avalanche Multiplication Suitable for Photon Counting
Masahide Goto and Shigeyuki Imura
NHK Science & Technology Research Laboratories, Tokyo, Japan,
- 16:12
R42 High-precision CMOS Proximity Capacitance Image Sensors with Large-format 12 μ m and High-resolution 2.8 μ m Pixels
Yuki Sugama¹, Yoshiaki Watanabe¹, Rihito Kuroda^{1,2}, Masahiro Yamamoto¹, Tetsuya Goto², Toshiro Yasuda³, Shinichi Murakami³, Hiroshi Hamori³, Naoya Kuriyama⁴, and Shigetoshi Sugawa²
¹ Graduate School of Engineering, Tohoku University,, Sendai, Miyagi, Japan
² New Industry Creation Hatchery Center, Tohoku University,
³ OHT Inc., ⁴ LAPIS Semiconductor Co. Ltd.
- 16:18
R43 A 4k by 4k 8000fps large format event-based sparse readout direct electron image sensor
Peng Gao¹, Benjamin Bammes², Sampsa Veijalainen¹, Jente Basteleus¹, Gaozhan Cai¹, Bert Luyssaert¹, Bart Dierickx¹, Robert Bilhorn²
¹ Caeleste, Mechelen, Belgium, ² Direct Electron LP, San Diego, CA, USA
- 16:24
R44 2x2-aperture 4-tap CMOS image sensor for multi-modal multi-band tissue imaging with suppressing the ambient light and motion artifact
Yuto Shimada, Kazuki Takada, Hoang Son Nam, Kakeru Miyazaki, Kohei Watanabe, Iori Shibata, Keita Yasutomi, Shoji Kawahito, Keiichiro Kagawa
Shizuoka University, Japan
- 16:30
R45 A Time-Resolved 4-tap Image Sensor Using Tapped PN-Junction Diode Demodulation Pixels
Hiroaki Nagae¹, Shohei Daikoku¹, Keita Kondo¹, Keita Yasutomi^{1,2}, Keiichiro Kagawa^{1,2}, Shoji Kawahito^{1,2}
¹ Graduate School of Integrated Science and Technology, Shizuoka University, Hamamatsu, Japan
² Research Institute of Electronics, Shizuoka University, Hamamatsu, Japan
- 16:36
R46 A 500x500 Dual-Gate SPAD Imager with 100% Temporal Aperture and 1 ns Minimum Gate Width for FLIM and Phasor Imaging Applications
Arin Can Ulku, Andrei Ardelean, Paul Mos, Claudio Bruschini, Edoardo Charbon
AQUA Laboratory, Ecole polytechnique fédérale de Lausanne (EPFL)
- 16:42 Break**

Session 10	Global Shutter and High Dynamic Range Imagers
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Session chair: TBA

- 16:48
R47 A 4.0 μ m Stacked Digital Pixel Sensor Operating in A Dual Quantization Mode for Over 120dB Dynamic Range
Kazuya Mori¹, Naoto Yasuda¹, Toshiyuki Isozaki¹, Ken Miyauchi¹, Isao Takayanagi¹, Junichi Nakamura¹, H.C.Chen², Ken Fu², SG.Wuu², Andrew Berkovich³, Song Chen³, Wei Gao³ and Chiao Liu³
¹ Brillnics Japan Inc., Tokyo, Japan
² Brillnics Inc., Hsinchu, Taiwan
³ Facebook Reality Labs, Facebook Inc, Menlo Park, CA, USA
- 16:54
R48 Automotive 3 μ m HDR Image Sensor with LFM and Distance Functionality
Sergey Velichko, Michael Guidash, Daniel Tekleab, Hung-Chih Chang, Manuel Innocent, Steve Nicholes, Denver Lloyd, Dave Jasinski, Andrew Perkins, Shaheen Amanullah, Maheedhar Suryadevara, Chris Silsby, Jeff Beck
Intelligent Sensing Group, ON Semiconductor, USA

- 17:00
R49 4.0 μ m Stacked Voltage Mode Global Shutter Pixels with A BSI LOFIC and A PDAF Capability
Ken Miyauchi[†], Kazuya Morit[†], Toshiyuki Isozaki[†], Yusuke Sawait[†], Ho-Ching Chien[#] and Junichi Nakamura[†]
[†]Brillnics Japan Inc., Tokyo, Japan
[#]Brillnics Inc., Hsinchu, Taiwan
- 17:06
R50 A 12Mpixel 1.3" optical format CMOS HDR image sensor achieving single-exposure flicker-free 90dB Dynamic range in GS shutter mode and 110dB Dynamic Range in 2-exposure ERS mode
Parthasarathy Sampath[†], Genis Chapinal^{}, Gurvinder Singh, Miten Odharia, Manuel Innocent, Tomas Geurts, Anirudh Oberoi, Rick Mauritzson, Chris Parks, John McCarten, Cristian Tivarus, Hung Doan, Neeraj Chouhan, Shreeshha Gopalakrishna, Dan Pates, Igor Butinar, Rajashekar Benjaram*
[†]ON Semiconductor, Bangalore, India
^{}ON Semiconductor, Mechelen, Belgium*
- 17:12
R51 A 5.6 μ m Stacked Voltage Domain Global Shutter Pixel with 70ke- Linear Full Well Capacity and 85dB Single Exposure High Dynamic Range
Yusuke Sawait[†], Toshiyuki Isozaki[†], Naoto Yasuda[†], Ken Miyauchi[†], Ken Fu[#] and Kazuya Morit[†] *[†]Brillnics Japan Inc., Tokyo, Japan* *[#]Brillnics Inc., Hsinchu, Taiwan*
- 17:18
R52 A Double Transfer 8.0 μ m Pixel with High Conversion Gain and Pixel Binning
Ikuo Mizuno¹, Masafumi Tsutsui¹, Masayuki Nakamura¹, Dmitri Ivanov², Dmitry Veigner² & Assaf Lahav²
¹Tower Partners Semiconductor Co., Ltd., Toyama, Japan
²Tower Semiconductor Migdal Haemeq 23105, Israel

17:24 Best Student Paper Award

Closing remarks