

[Invited] Journey of pixel optics scaling into deep sub-micron and migration to meta-optics era

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Abstract This paper reviews pixel optics trend of current CMOS image sensors. In order to pack more pixels into compact form factor of high-resolution mobile cameras, pixel scaling continues into deep sub-micron size. Evolution of pixel optics technology to compensate inherent performance drop from pixel scaling is explained, and the introduction of meta-optics technology to overcome the performance limit of conventional optics is discussed.

Keywords: CMOS image sensor, color filter isolation grid, micro-lens, nano photonics, meta-surface, meta-materials, meta-optics, nano-prism, meta-prism

1. Mobile camera and market trend

CMOS image sensor (CIS) market is recovering slowly after pandemic thanks to the number of cameras per mobile phone keeps increasing. In addition, there is a distinct trend of more high resolution cameras are adopted, such as recent 200MP [1], to provide superior image quality. In order to pack more pixels into compact form factor of slim mobile phones, pixel scaling is inevitable, and deep sub-micron pixel, for example 0.5 μm is presented recently [2]. This trend would continue as the demand for thin camera modules increases due to foldable phones are gaining popularity in the market.

2. Journey of pixel optics scaling

Fundamentally, the sensitivity of each pixel drops as the pixel area decreases. Pixel optic architecture has been evolved to increase the signal (received photons) and reduce the noise (optical crosstalk) so that signal-to-noise ratio (SNR) remains comparable even with the pixel scaling: from front-side illumination (FSI), light guide [3], back-side illumination (BSI) [4], backside deep-trench isolation (DTI) [5] to full-depth DTI [6]. Although the architectural evolution, one-micron pixel was not enough for the main camera until the merged color filter technology, such as tetra-pixel (2x2) could provide both full-resolution image with re-mosaic image signal processing (ISP) technique when bright, and brighter image at dark conditions [7]. This technology continues to extend toward Tetra²-pixel (4x4) [8] enabling deep sub-micron pixel scaling such as 0.5 μm [2].

A major challenge to maintain reasonable sensitivity at sub-micron pixel is from the diffraction limit of the micro-lens. Because the beam spot size does not scale as pixel scales, conventional metal color-filter isolation grid interacts more with incoming light, for example, 32% at 0.7 μm pixel case, which results in optical loss [9]. Metal-free, low-refractive index dielectric-based grid technology solved this problem [9], and extends to air-gap grid technology [10, 11].

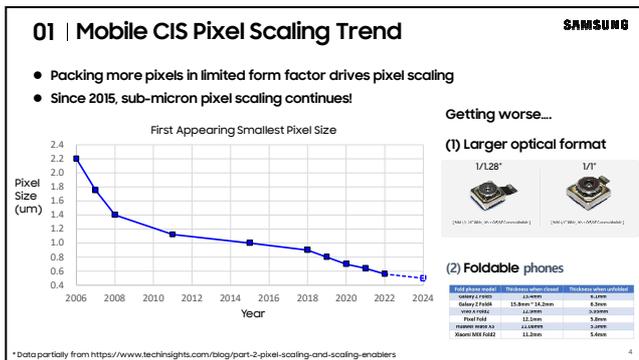
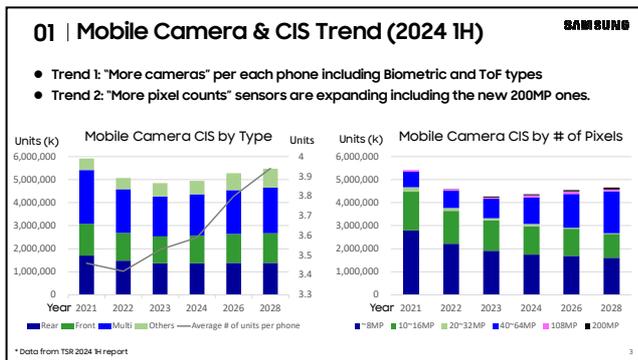
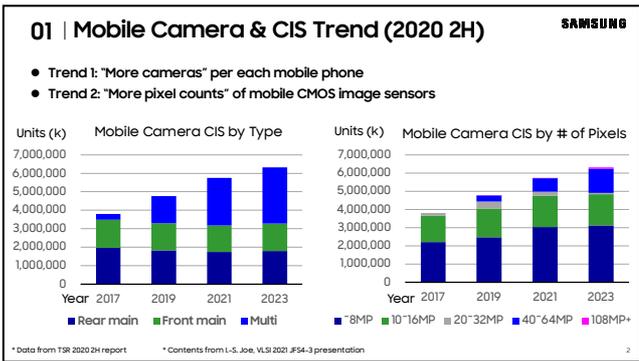
3. Migration to meta-optics

There is a clear wall of maximum sensitivity at given pixel size with the conventional optic structure, which relies on micro-lens and color filters. More than half of incoming light is absorbed at the color filter in case of a green pixel. In order to overcome this sensitivity wall, recent attempts to adopt meta-optics technology show promising results [12-14]. Proposed nano-prism functions as a color router and larger-than-pixel lens to scavenge more light from nearby color pixels for improved sensitivity (+25%) [13]. Still in early stage of application to sensors, meta-optics showed potentials to provide other interesting opportunities such as extreme pixel scaling (0.22 μm pixel pitch) [15], and improved color accuracy at 0.22 μm pixel pitch [16].

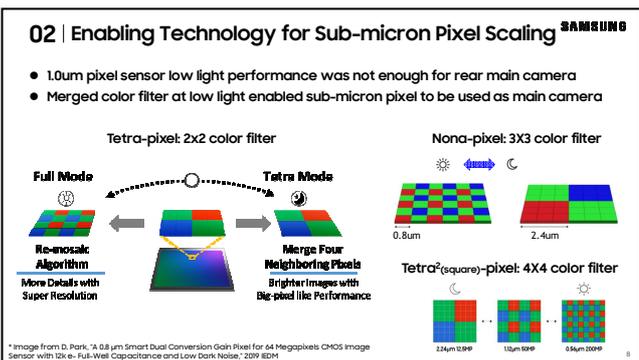
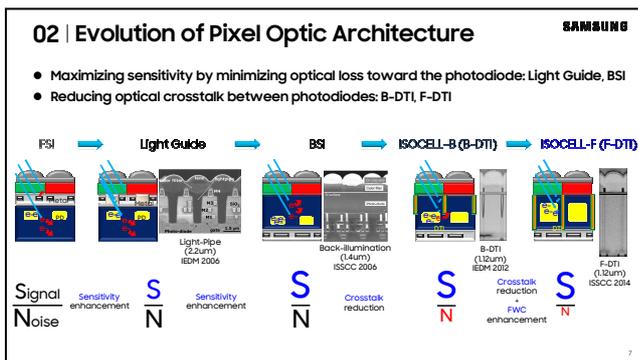
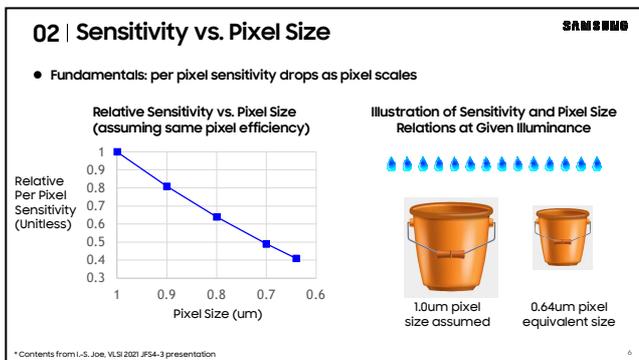
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Mobile camera & market trend



Journey of pixel optics scaling



02 | Sub-micron Scaling Challenge: Micro-lens Focus Spot Size

- Color-filter grid (conventionally Tungsten) introduced to suppress crosstalk between nearby pixels, but light-to-grid interaction increases as pixel scales into sub-micron

Calculated Beam Intensity

FDTD Simulated Beam Intensity

* Contents from L.-S. Joe, VLSI 2021 #54-3 presentation

02 | Color-Filter Grid for Sub-micron Optical Scaling

- Optical loss from absorption increases as more light interacts with conventional metal color-filter grids (32% at 0.7um pixel)
- Hybrid metal-and-dielectric grid structure introduced at 0.8um pixel generation

Optical Absorption Break-down

Color-filter Grid Comparison

FDTD Simulation

* Contents from L.-S. Joe, VLSI 2021 #54-3 presentation

02 | Metal-free Low-RI Grid for 0.64um Pixel

- New metal-free, low-refractive-index dielectric material based grid structure designed and implemented with 0.64um pixel prototype

Schematic of Hybrid Metal-and-dielectric Grid and TEM Image of Fabricated Prototype

Schematic of Metal-free Low-RI Dielectric Grid and TEM Image of Fabricated Prototype

* Contents from L.-S. Joe, VLSI 2021 #54-3 presentation

02 | Metal-free Low-RI Grid Measurement Results

- Prototype sensor with new metal-free low-RI dielectric grid compared with previous hybrid grid type → 29% improved sensitivity

Measured Q.E. (normalized) Spectrum Comparison of Hybrid Grid and Metal-free Grid

Performance Comparison of Hybrid Grid and Metal-free Grid Prototype Sensors (0.64um)

	Hybrid grid	Metal-free grid
Normalized Q.E. (Green)	100 %	123 %
Sensitivity (Green)	1,190 e-/Lux.s	1,530 e-/Lux.s (+29%)
Y-SNR	29.5 dB	30.7 dB
Crosstalk	18.4%	18.5%

* Contents from L.-S. Joe, VLSI 2021 #54-3 presentation

02 | What's Next? Air-gap Grid!

- Next stage for low RI color filter grid is introducing air (n=1.0) into grid structure
- Successful demonstration of air-gap grid is done at 0.56um pixel prototype (ISSCC 2022)

✓ Problem: Pixel shrink → poor sensitivity & crosstalk
 Solution: Lower RI grid → TIR → ① sensitivity ▲ & ② crosstalk ▼

✓ Air-gap (RI ▼) → better optical performance

Crosstalk	1.2% ▼
Q.E	7 % ▲
Sensitivity	5.5% ▲
YSNR	0.3 dB ▲

* Contents from S. Park, ISSCC 2022 5.8 presentation

02 | Air-gap Grid Example

- Another air-gap grid technology is presented at IEDM 2022: two types for 0.7um and 0.64um pixel sensors

(a) Air-metal grid (b) Air grid

(a) Air-metal grid (b) Air grid

Fig. 6. Cross-sectional transmission electron microscopy (TEM) images of (a) the air-metal grid and (b) air grid.

✓ Air-metal grid for 0.7um pixel
 ✓ Air grid for 0.64um pixel

Fig. 8. Measurement results of (a, b) white image and (c) cross shading for the air-metal grid and air grid.

* Images from H. Bak, 'Advanced Color Filter Isolation Technology for Sub-Micron Pixel of CMOS Image Sensor', IEDM 2022

02 | Evolution of Pixel Optic Structures: DTI

- Poly-Si used to suppress dark noise at DTI to photodiode interface by negatively bias it
- Optical loss from poly-Si gets larger as pixel scales, and introduction of quad PD (Q-cell) type
- New DTI structure made of "oxide / poly-Si / oxide / poly-Si / oxide" demonstrated improved sensitivity at 0.5um Q-cell type pixel

Quad PD (Q-cell)

Previous work

This work

* Images from D. Kim, ISSCC 2024 6.10 presentation

02 | Conventional Pixel Optics Performance Break-down

- Recent conventional pixel optics technology reached near-saturated sensitivity
- New approach to fight color filter optical loss would be the next breakthrough

Breakdown of optical loss (Green pixel)

* Contents from D. Kim, ISSCC 2024 6.10 presentation

Migration to meta-optics

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03 | Introduction of Meta-Optics

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- Metasurface controls amplitude, phase, and polarization of light using array of sub-wavelength structures

Filter

Nano Lett. 17, 5, 3195-3164 (2017)
ACS Photon. 4, 4, 1026-1025 (2019)

Metasurface

Science 352, 6290, 1190-1194 (2016)

Sensor

Nat. Commun. 9, 4196 (2018)
Adv. Func. Mater. 32, 5, 2106550 (2022)

Imaging

Nat. Commun. 11, 3205 (2020)
Nat. Commun. 9, 4562 (2018)
Nanoscale 10, 4237-4245 (2018)

03 | Application of Meta-Optics to CIS

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- Nano-Prism is the first trial of meta-optics application to product-level CIS (IEDM 2021)
- Nano-Prism functions as color router and big micro-lens to improve sensitivity

Optical phase control using Nano-rods

Green Blue

Conventional micro-lens vs. Nano-Prism

~2/3 light loss by color filter absorption

Better sensitivity by routing more light from near-by pixels

* Images from S. Yun, "Highly Efficient Color Separation and Focusing in the Sub-micron CMOS Image Sensor", IEDM 2021

03 | Nano-Prism Working Principles

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- Diffractive lens and color outer functions are designed using array of different size nano-rods

Diffractive lens function

Focal length

Color routing function

$\lambda_1 < \lambda_2 \rightarrow \theta_1 < \theta_2$

* Contents from C. Choi, "EDM 2023 8.3 presentation"

03 | Nano-Prism Design and Results

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- Designed nano-prism gives better sensitivity (>25%) over conventional micro-lens

No lens Micro-lens Nano-Prism

Pitch: 128 μm
@ 530 nm

Spectral response comparison

Normalized QE vs wavelength [nm]

* Contents from C. Choi, "EDM 2023 8.3 presentation"

03 | Nano-Prism Design Examples

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- Meta-optics can provide different spectral response results only by changing the nano-rod array design \rightarrow Design freedom that is not limited by fabrication capabilities

Uniformly Weighted

Green & Blue Weighted

Green Weighted

* Contents from C. Choi, "EDM 2023 8.3 presentation"

03 | Meta-Optics Example

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- Another meta-optic technology, named "Nano light pillars" is presented at IEDM 2023

Nano light pillars concept

Spectral response

Fabricated NLP structure

* Images from C.-Y. Wang, "CMOS Image sensor with nano light pillars for optical performance enhancement", IEDM 2023

04 | Outlook - Extreme Pixel Scaling

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- Deep sub-micron (0.22 μm) pixel pitch studied (Nature Comm. 2020): Si photodiode (PIN Si rod) is placed at each color resonance (R/Y/G)

Si nano-rods are used for photodiode and color selection

Stronger resonance at smaller pixel pitch (350nm(c,d) vs. 220nm(e,f))

Fabricated device

* Images from J.S.T. Smalley, "Sub-wavelength pixelated CMOS color sensors based on anti-Hermitian metasurface", Nature Comm. 2020